

A block diagram of a computer system architecture. A central horizontal bus (112) connects various components. On the left side of the bus, from left to right, are the CPU (110), ROM (116), RAM (114), and I/O ADAPTER (118). The I/O ADAPTER (118) is connected to a disk drive (120) and a NETWORK (135) via a lightning bolt symbol (134). On the right side of the bus, from left to right, are the USER INTERFACE ADAPTER (122), a DISPLAY ADAPTER (136), and a COMMUNICATION ADAPTER (134). The USER INTERFACE ADAPTER (122) is connected to a keyboard (132), a mouse (126), and a monitor (128). The DISPLAY ADAPTER (136) is connected to a monitor (138). The COMMUNICATION ADAPTER (134) is connected to the NETWORK (135).

Figure 1

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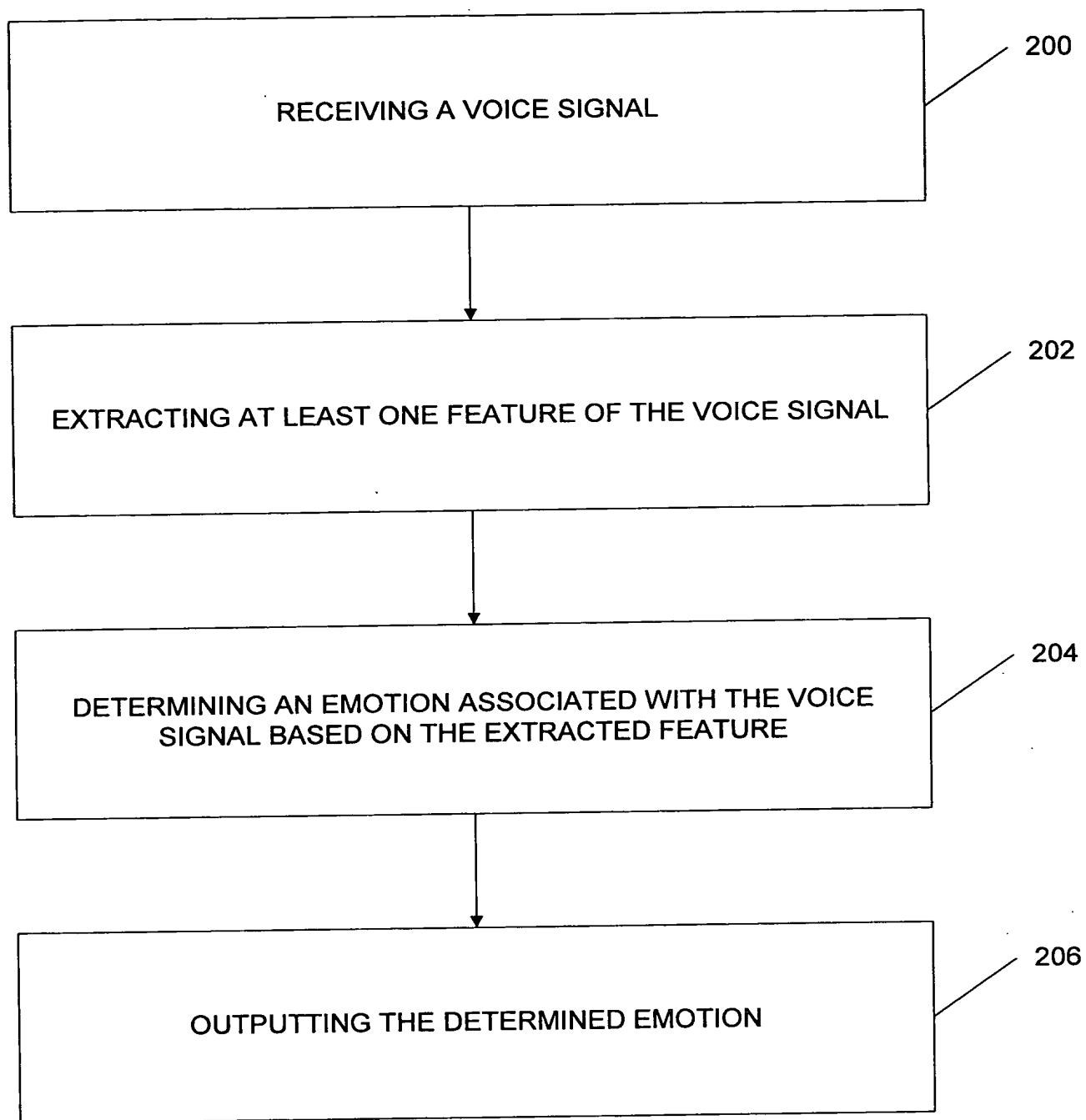


Figure 2

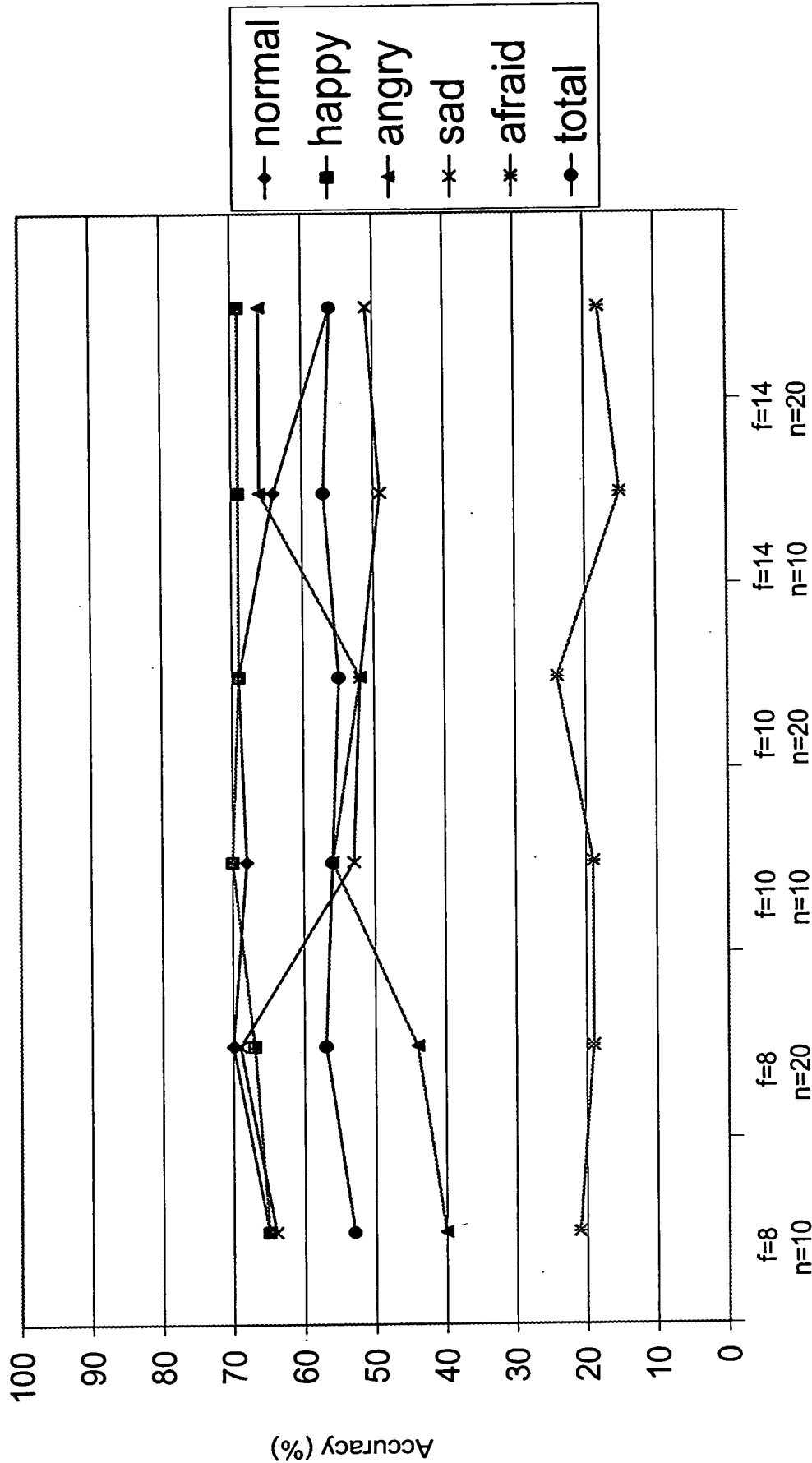


Figure 3

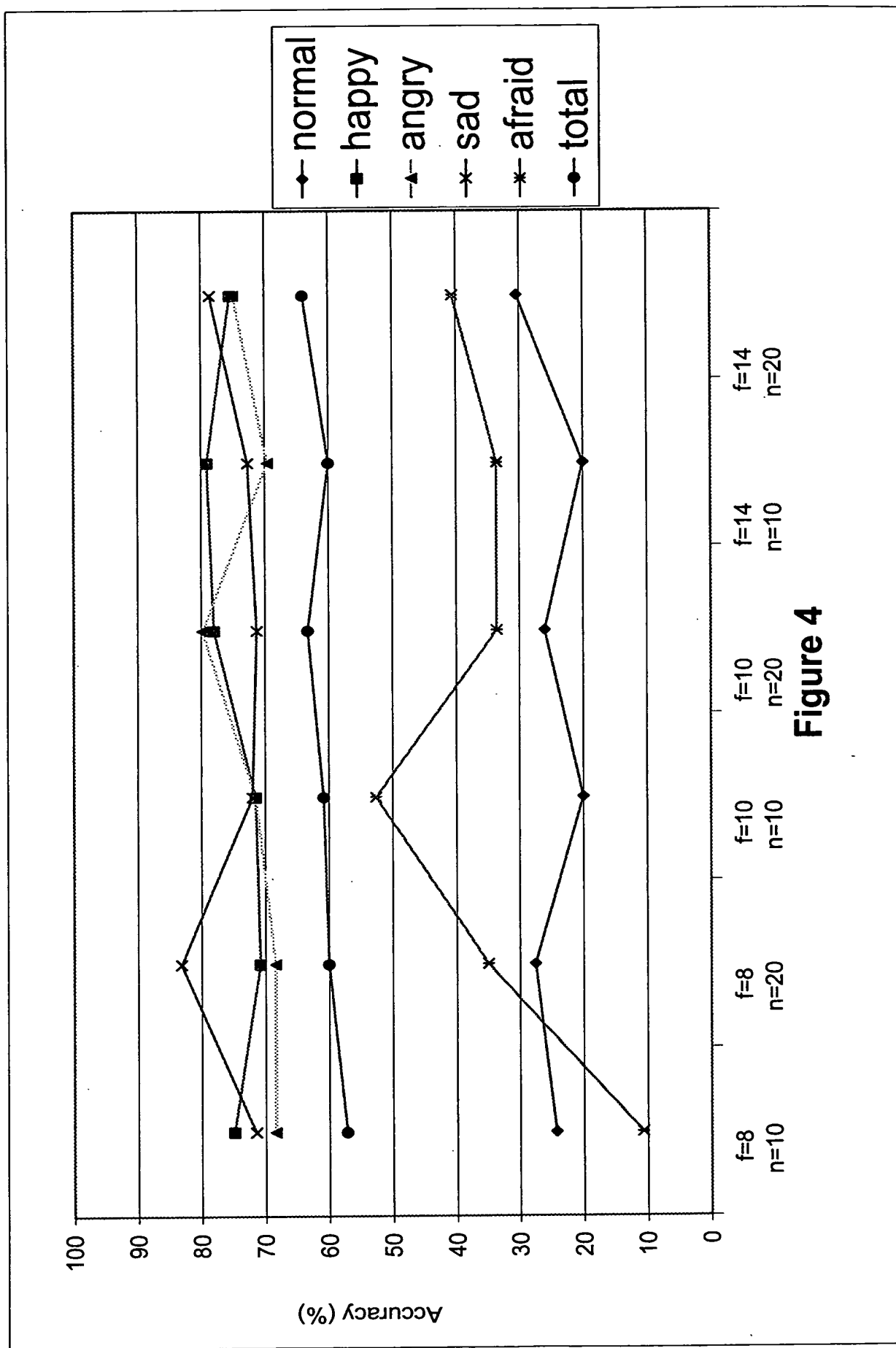


Figure 4

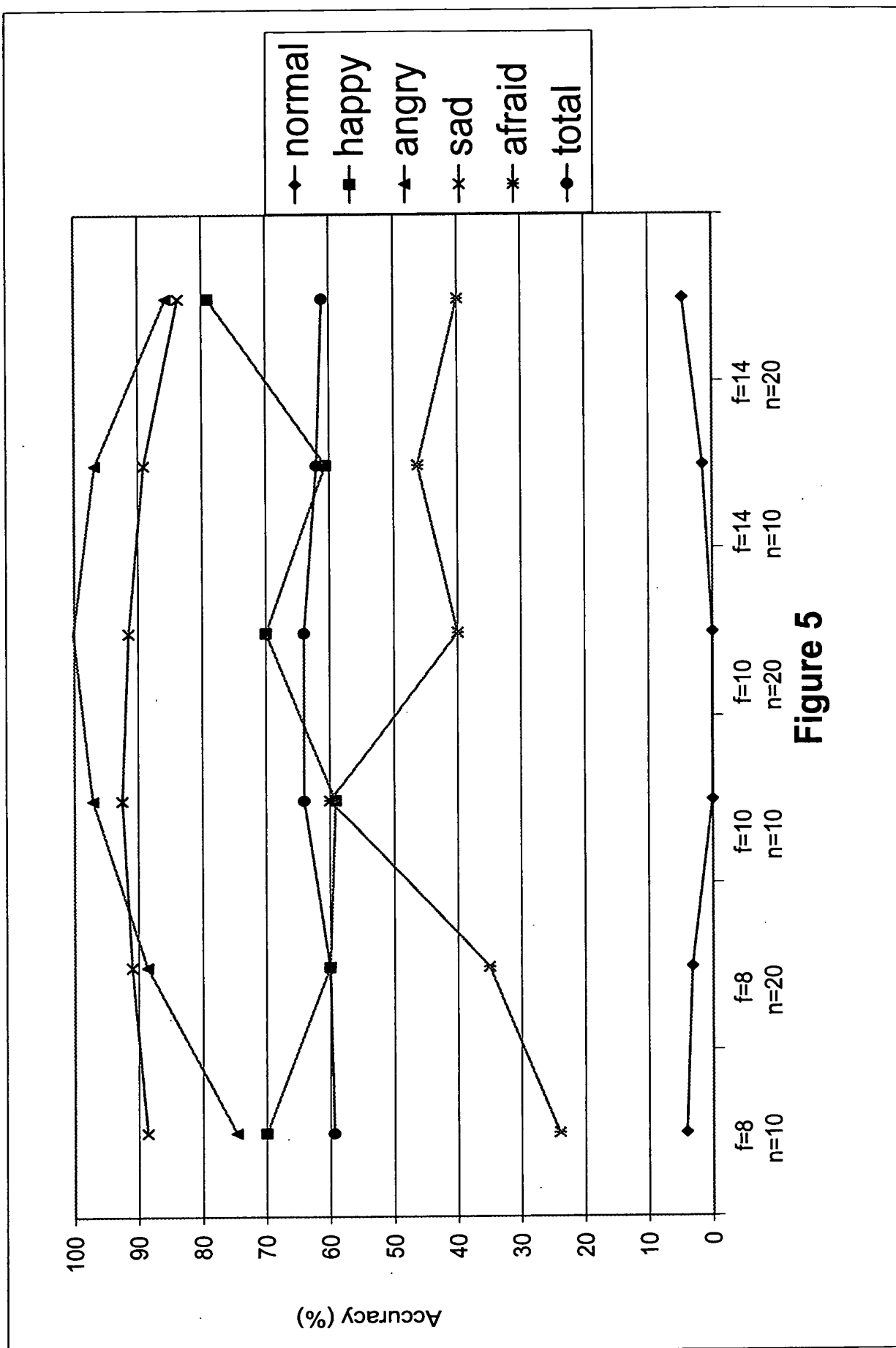


Figure 5

600 602 604 606 608 610

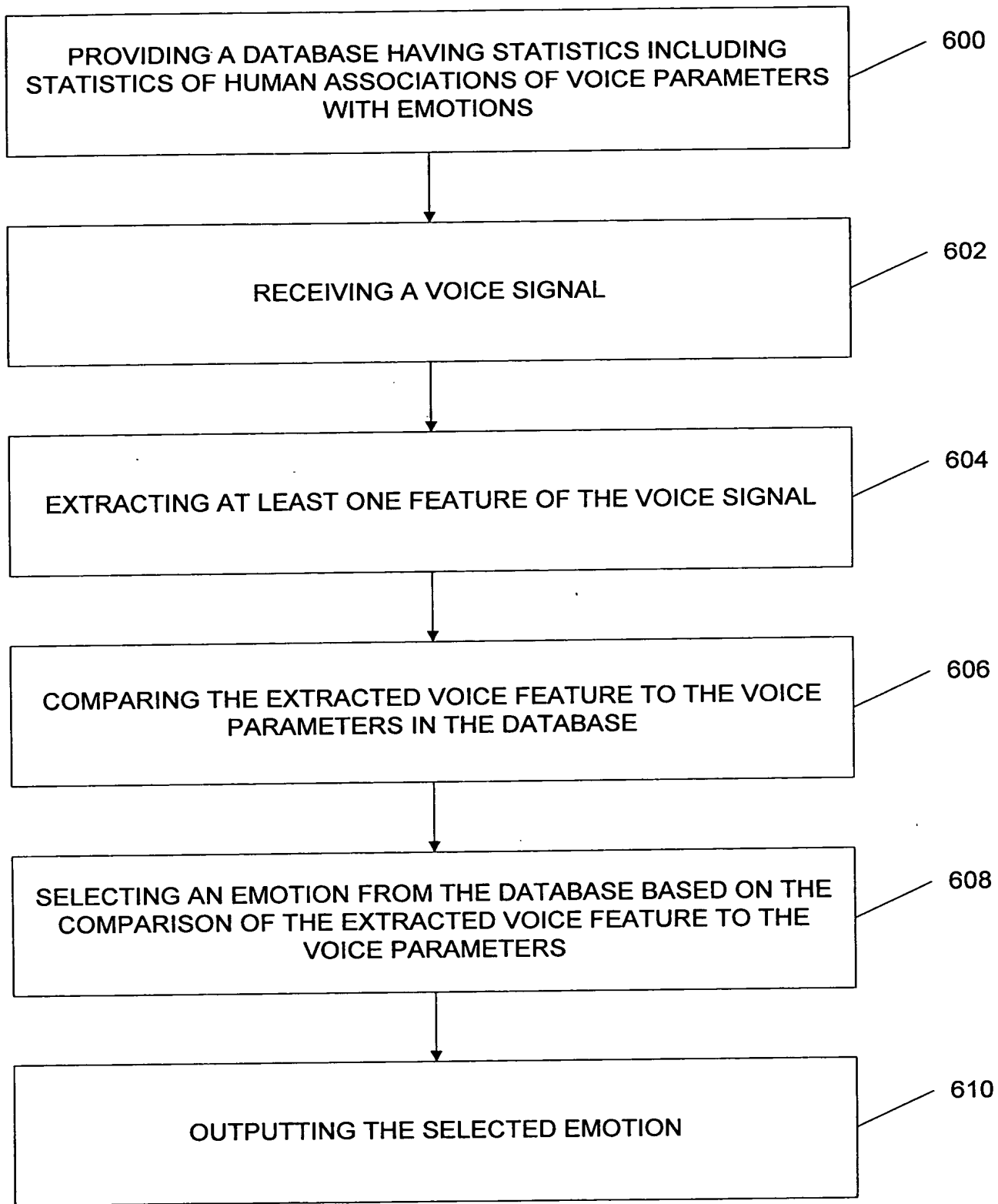


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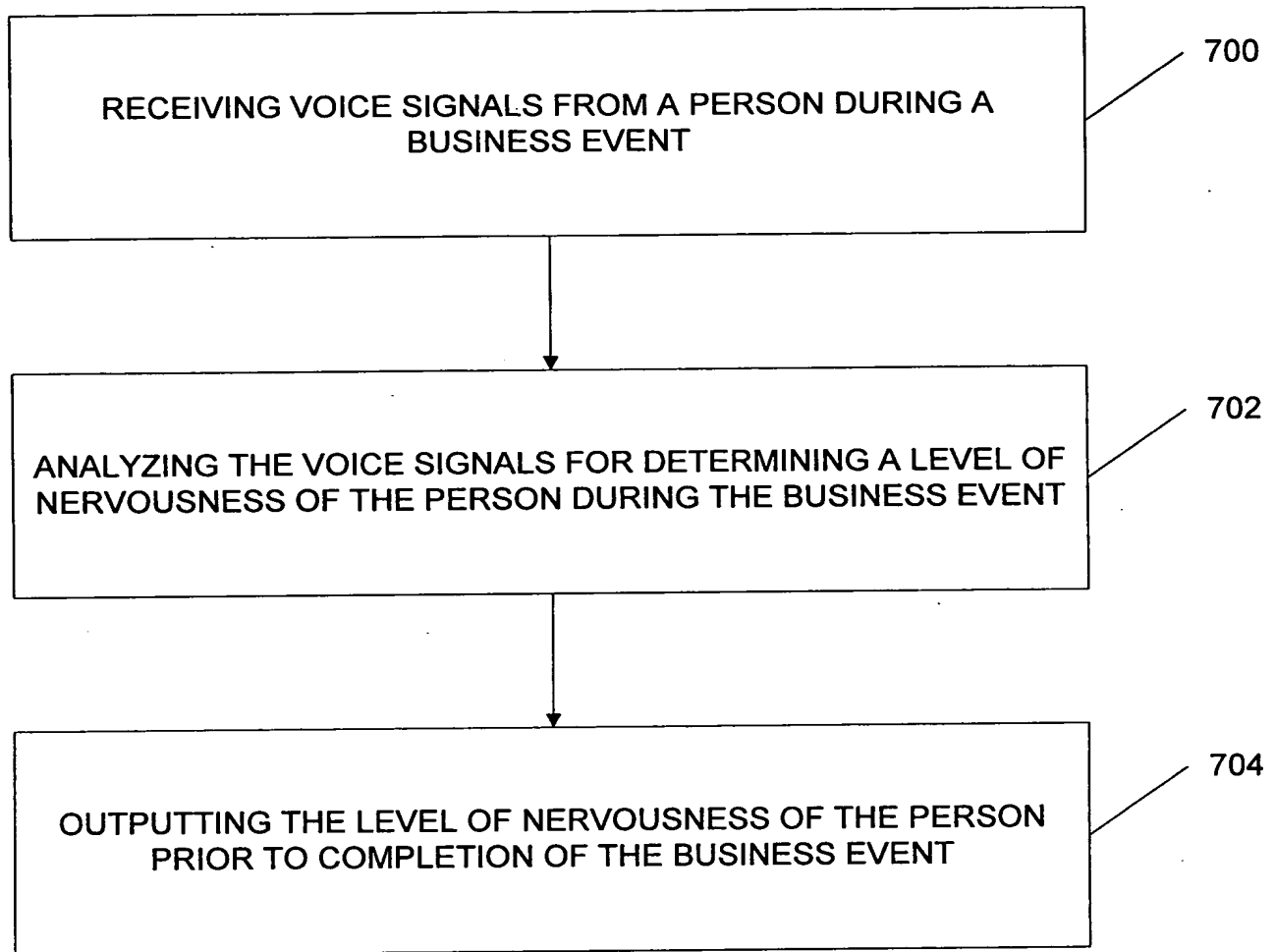


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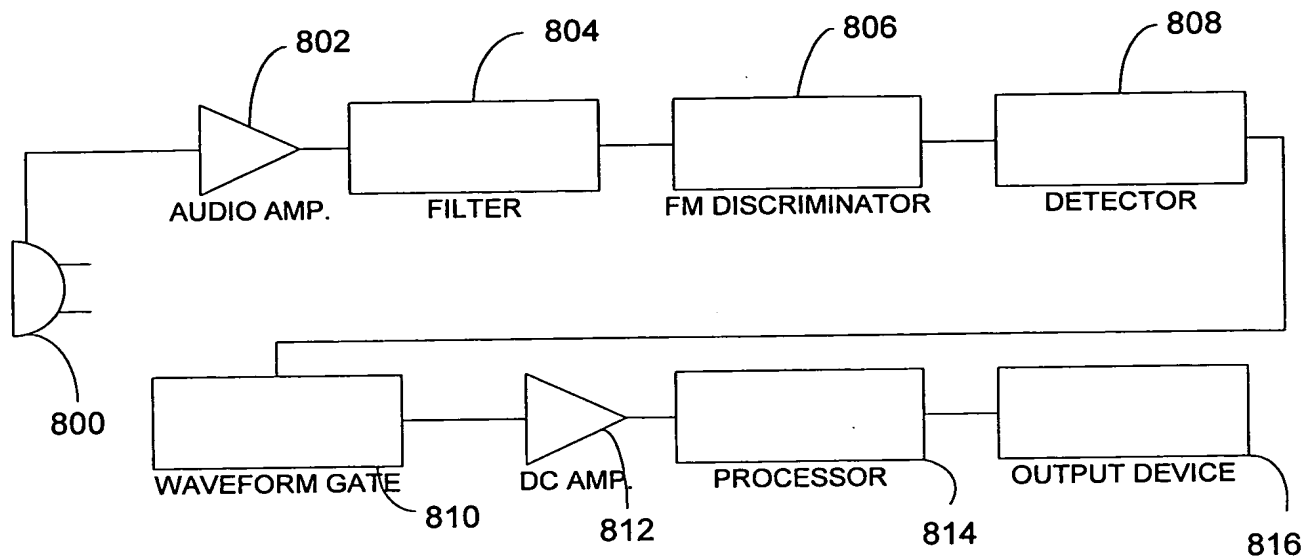


Figure 8

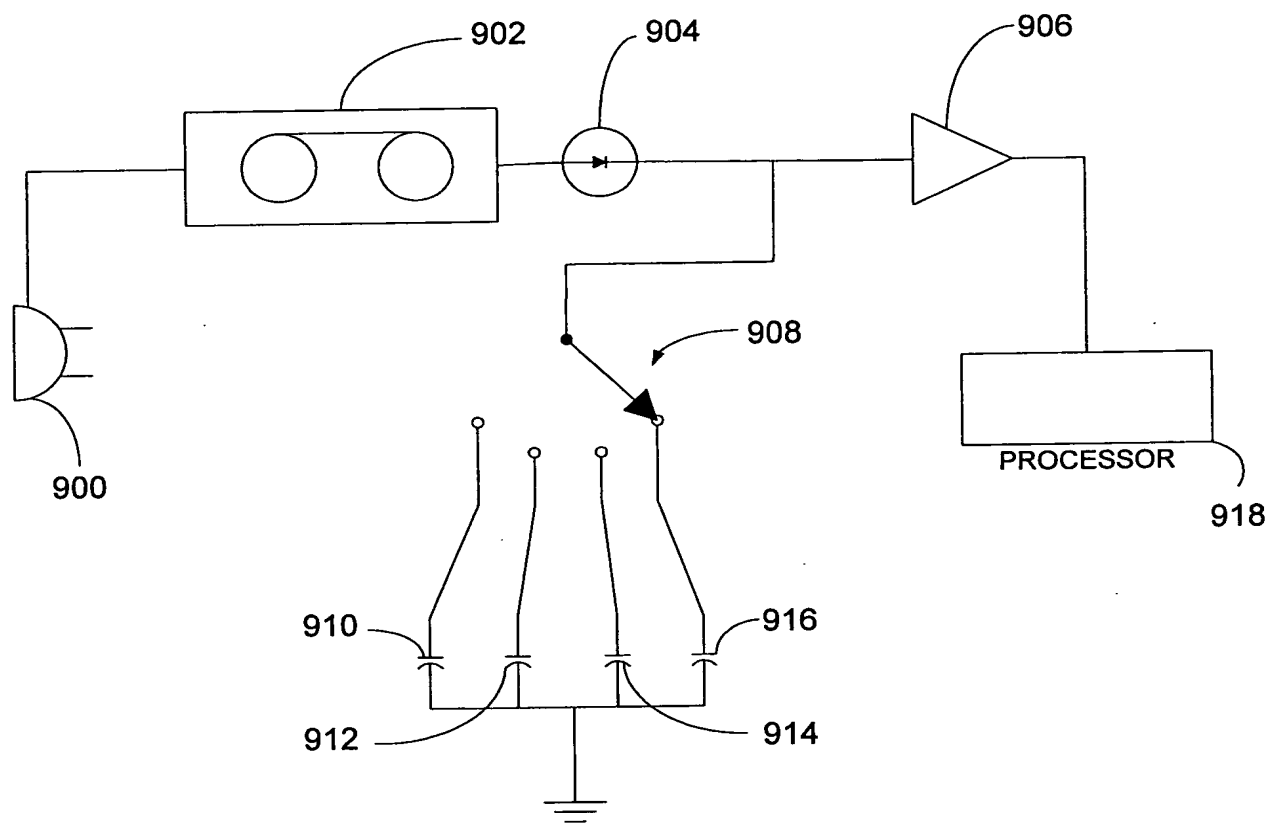


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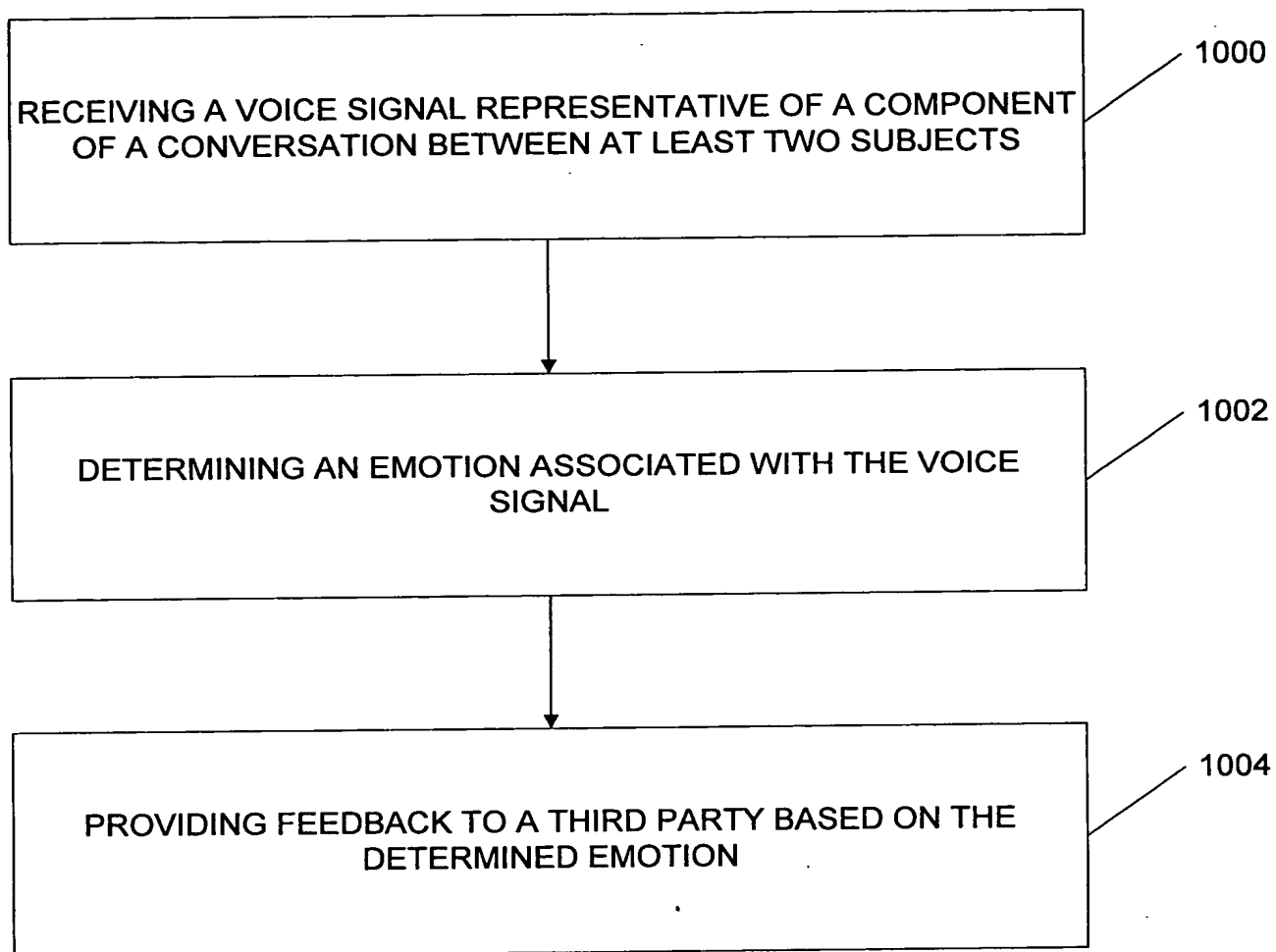


Figure 10

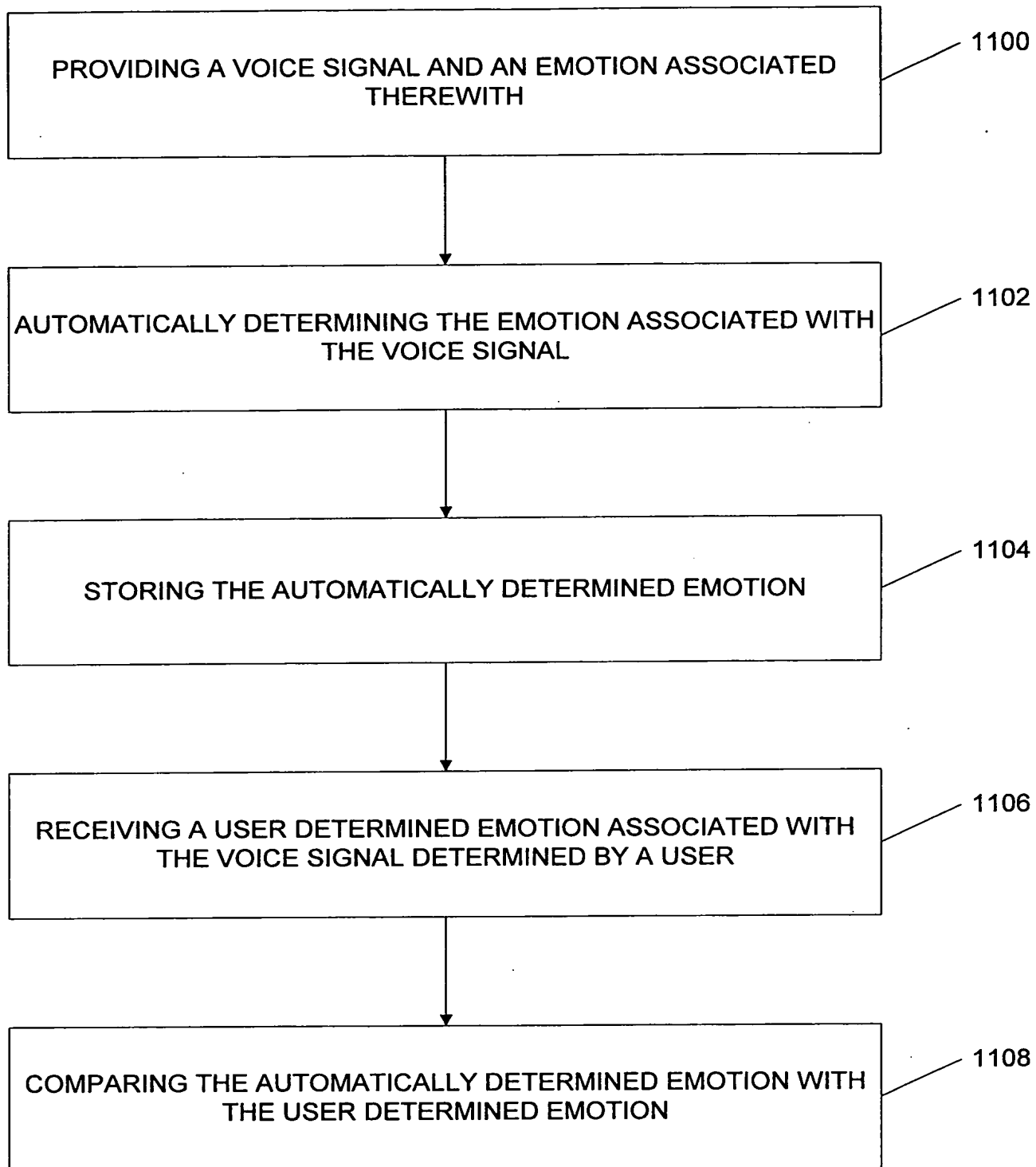


Figure 11

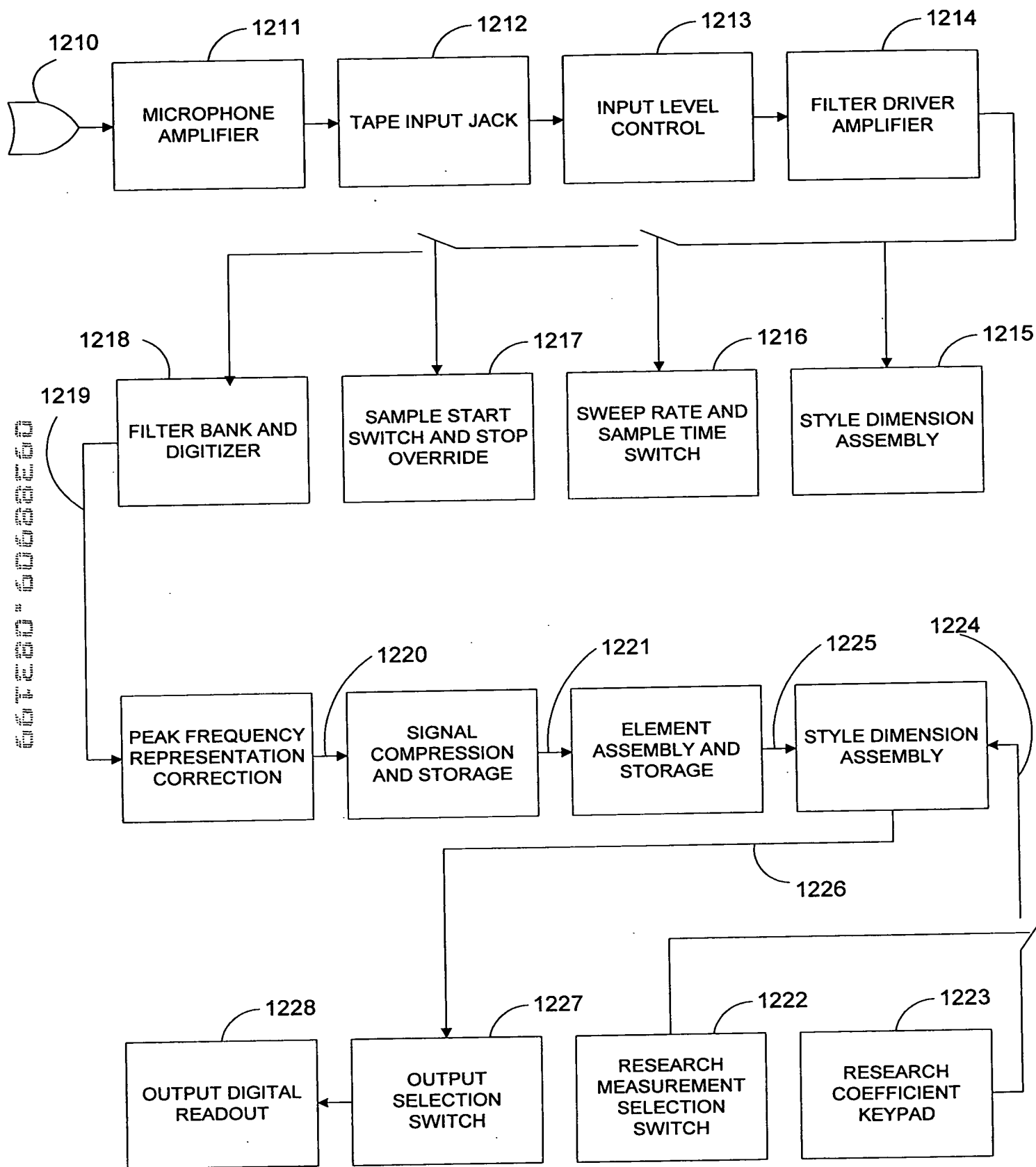


Figure 12

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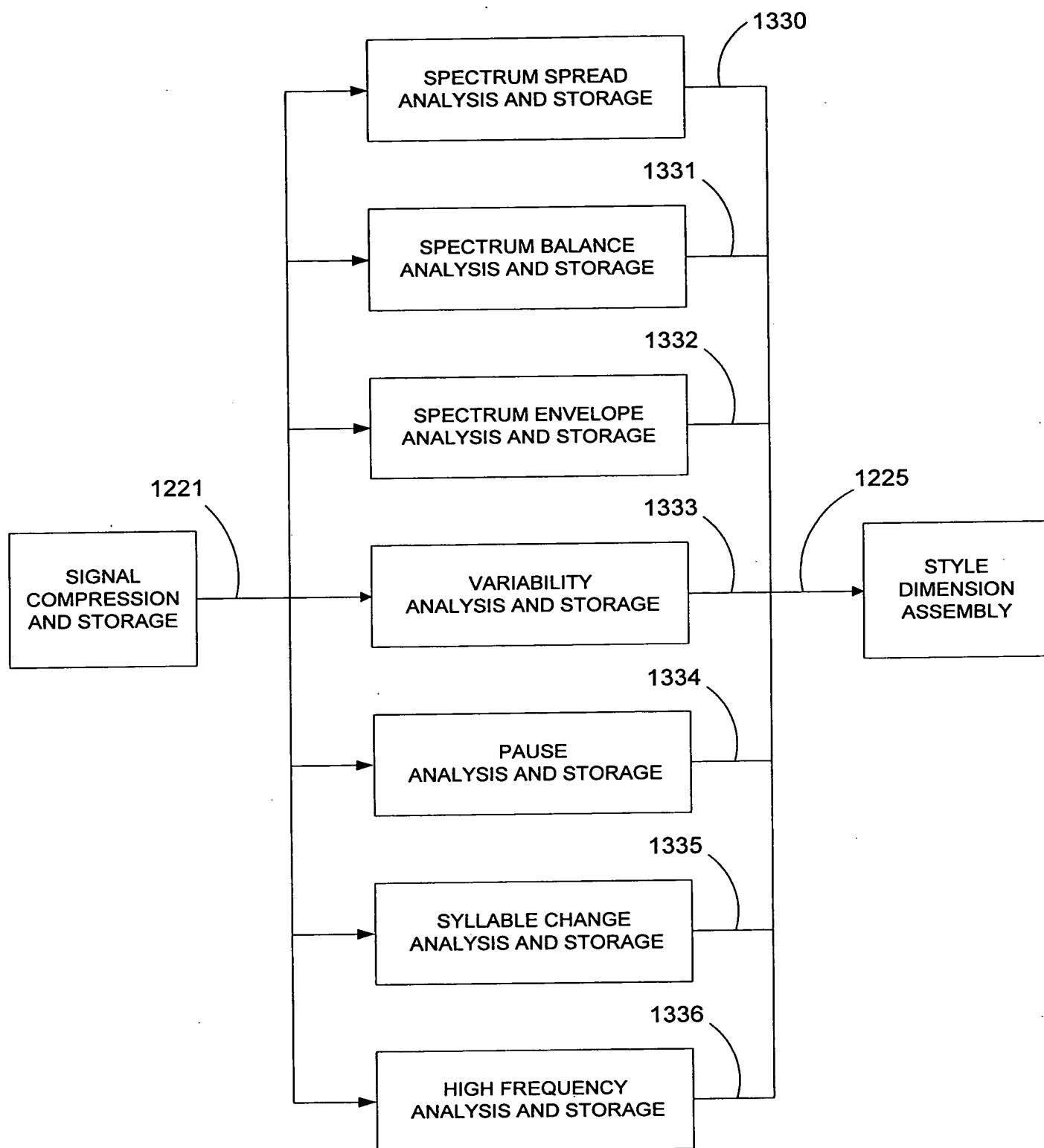


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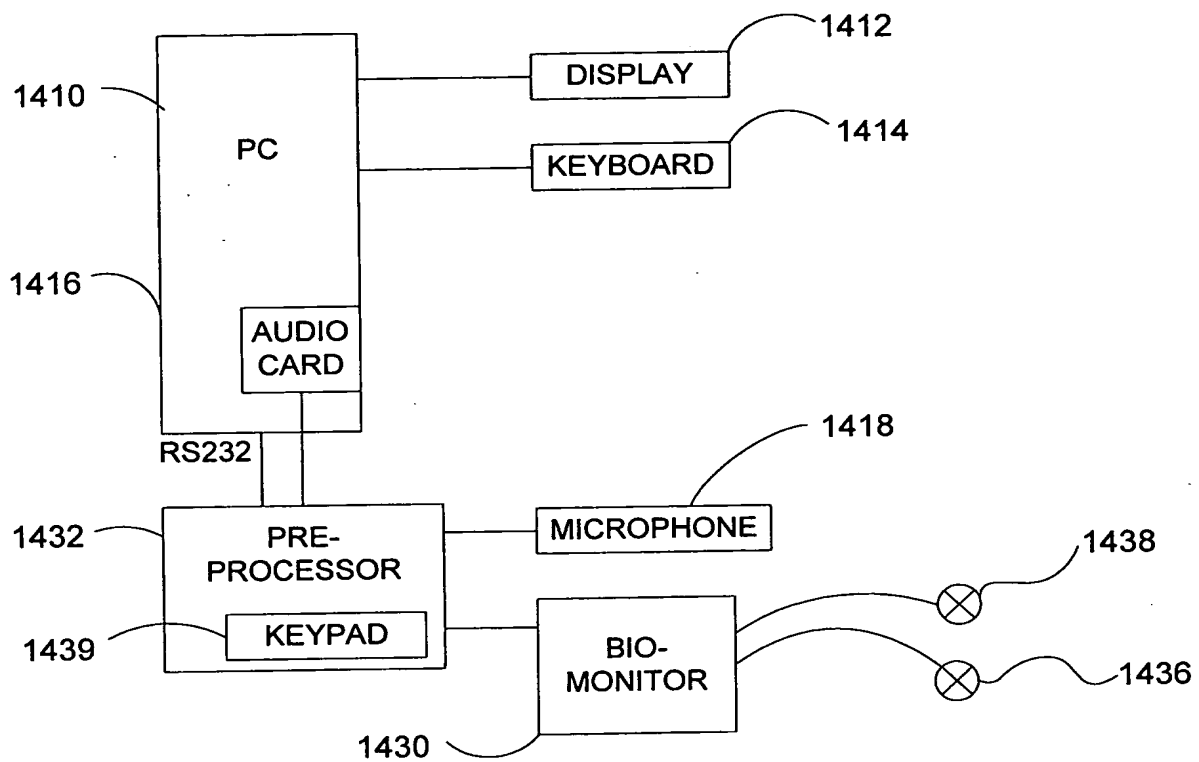


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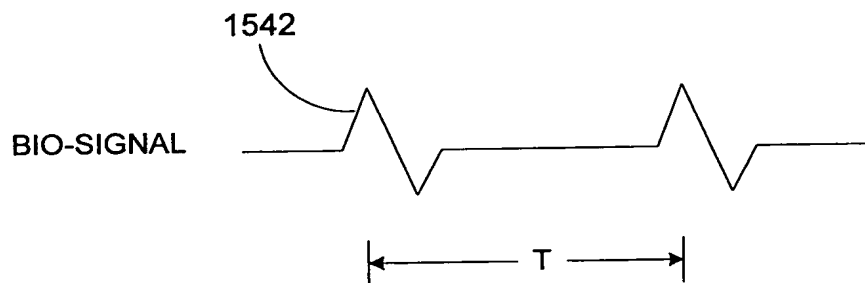


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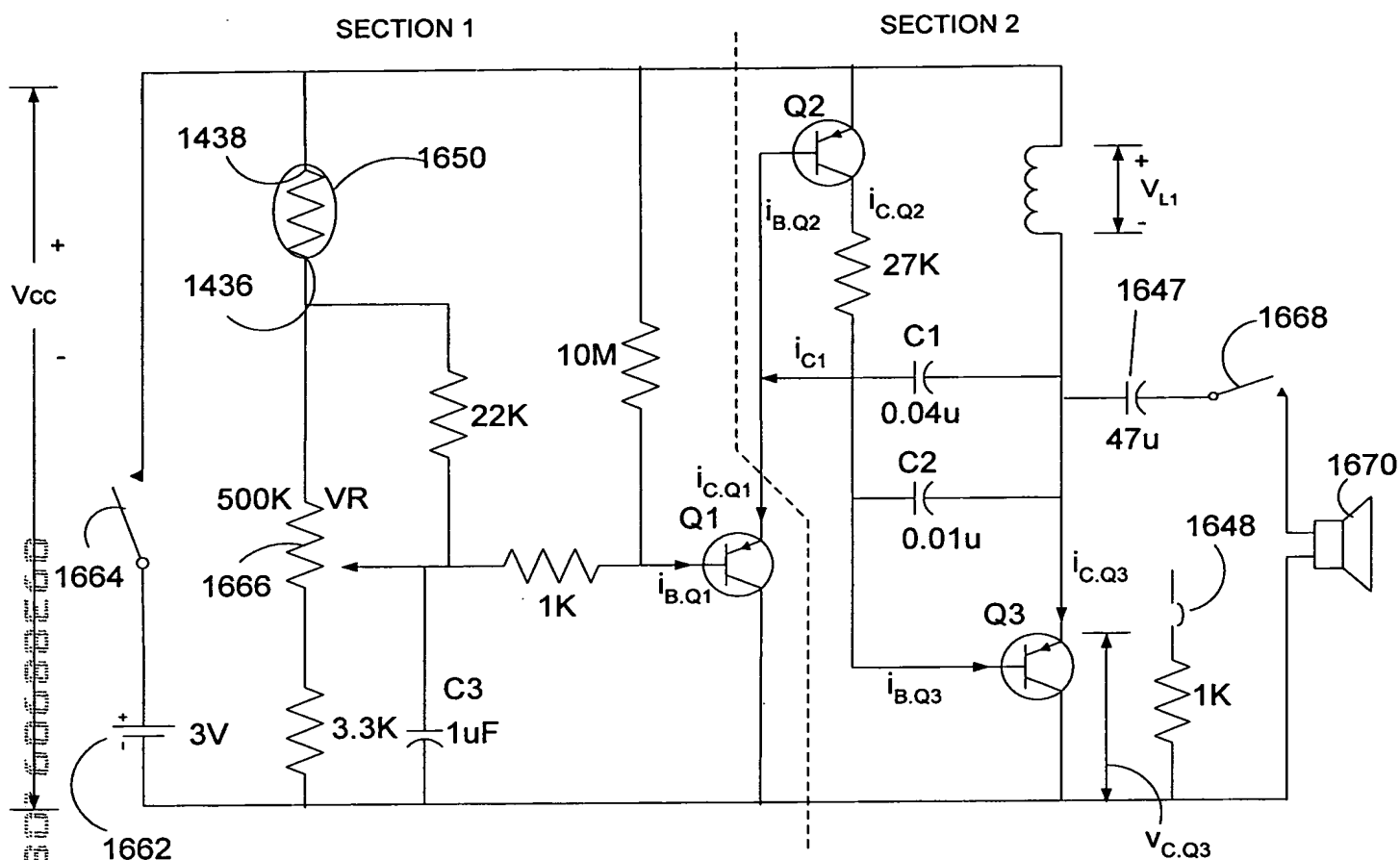


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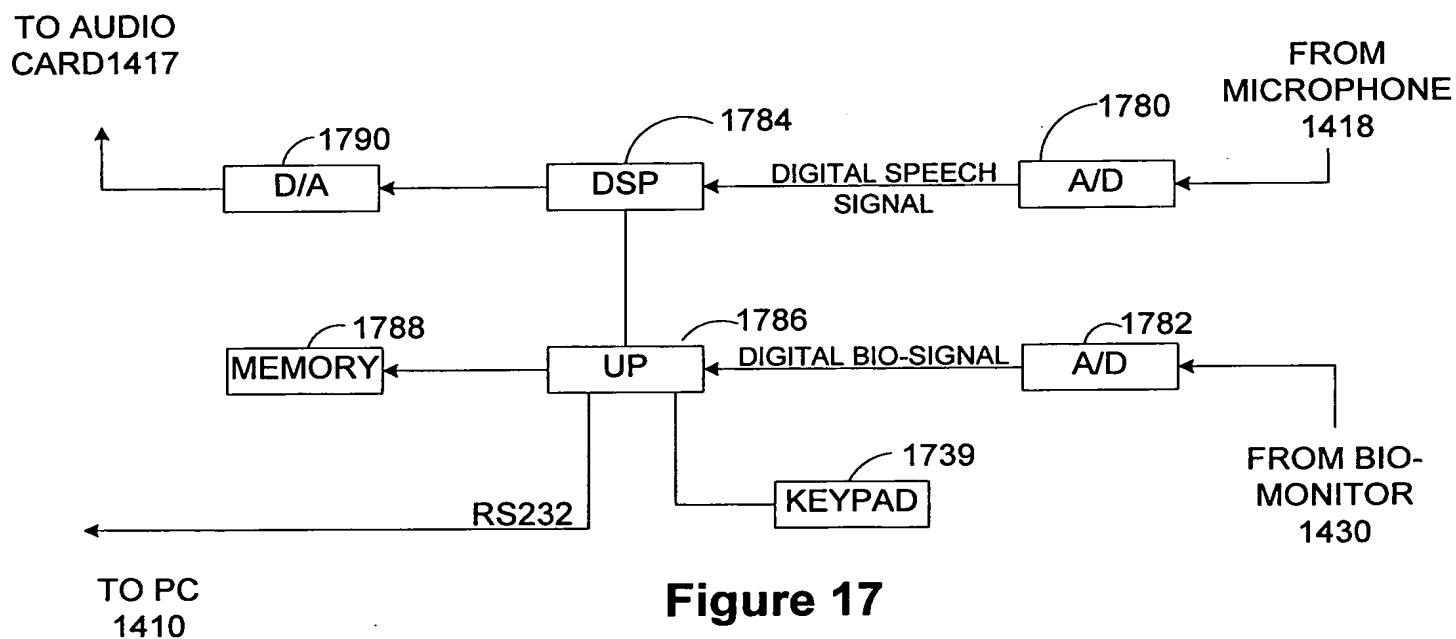
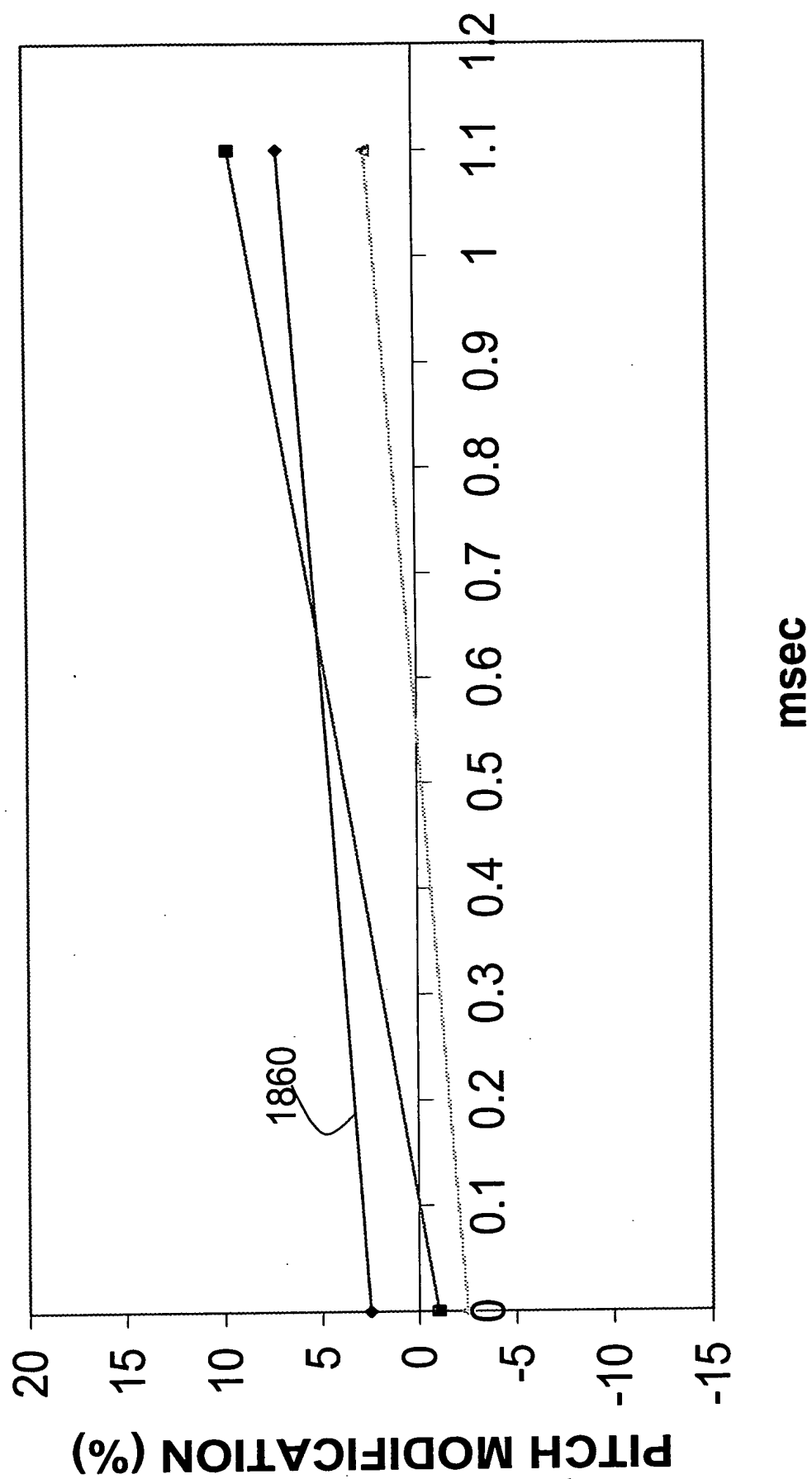


Figure 17

Figure 18



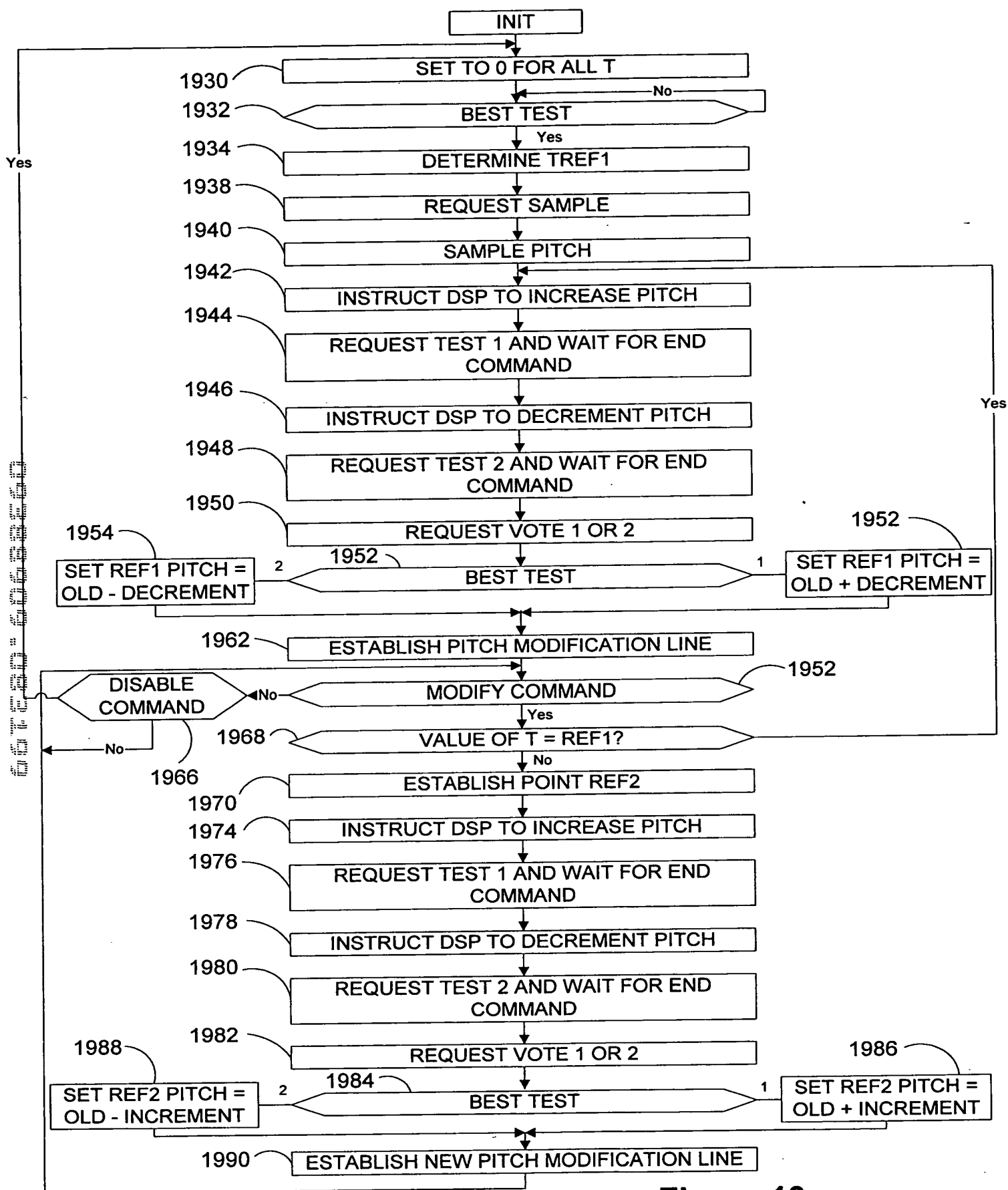


Figure 19

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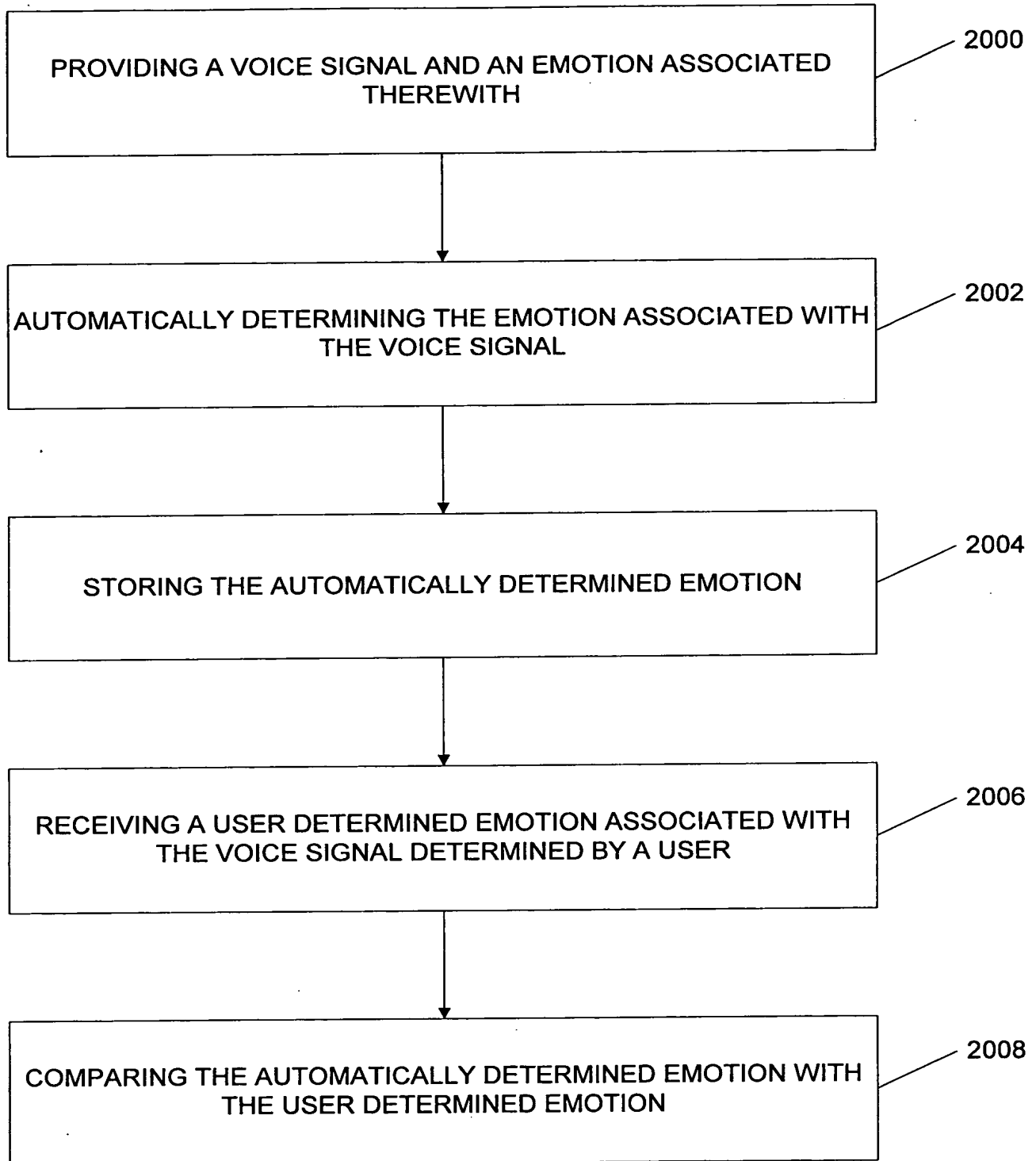


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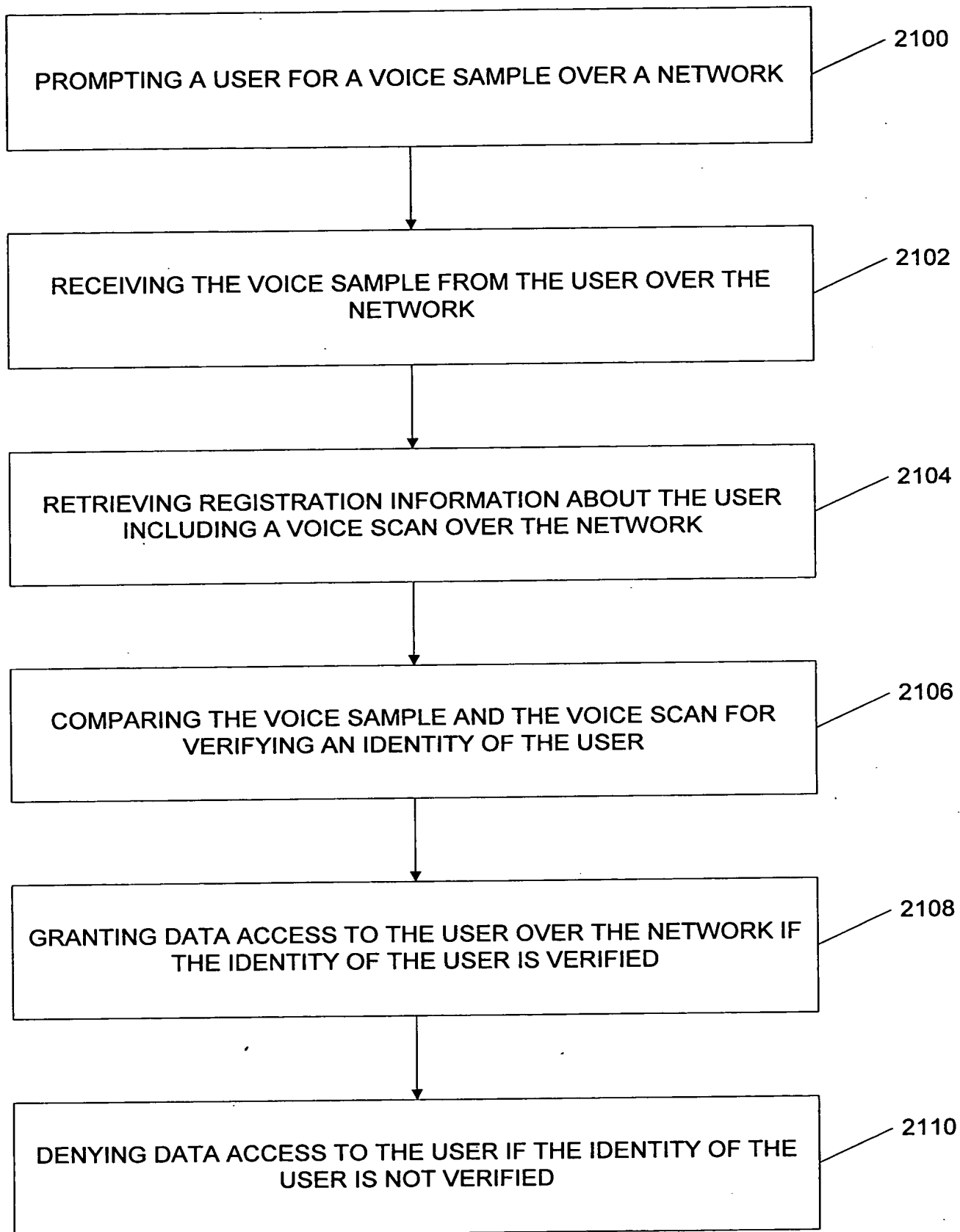


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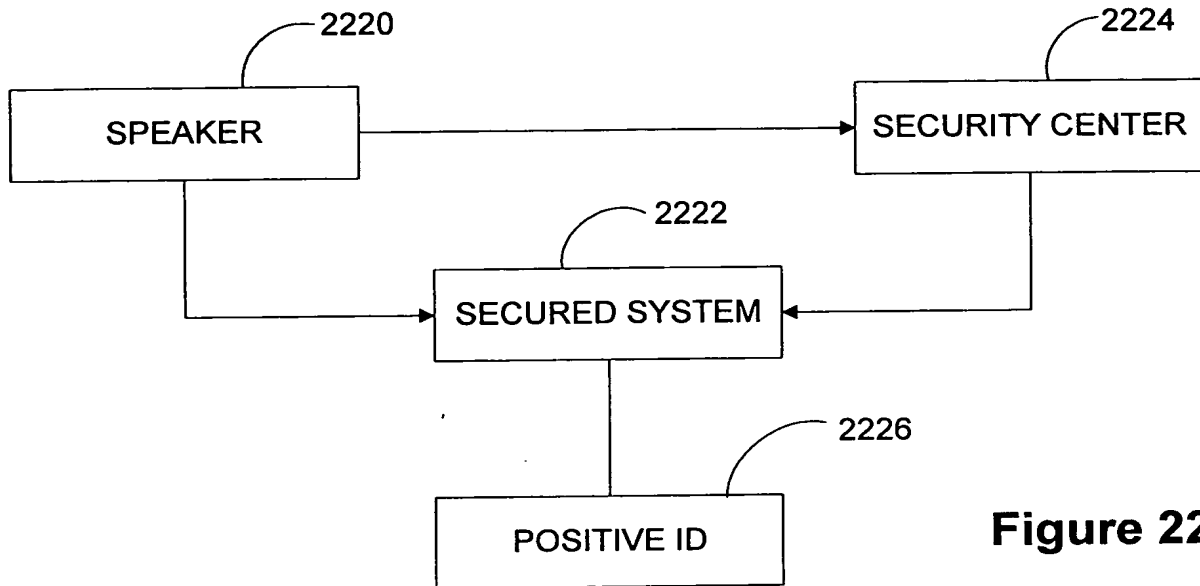


Figure 22

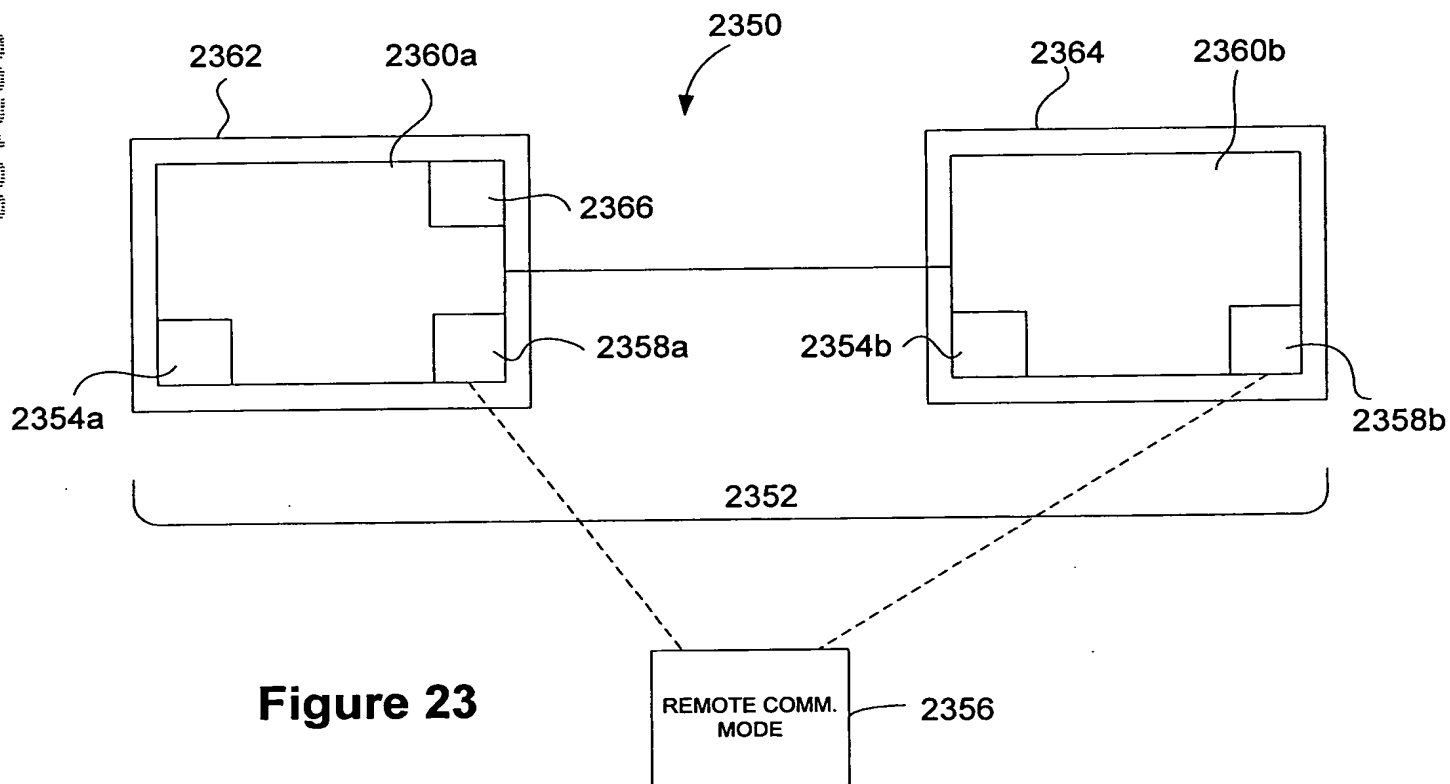
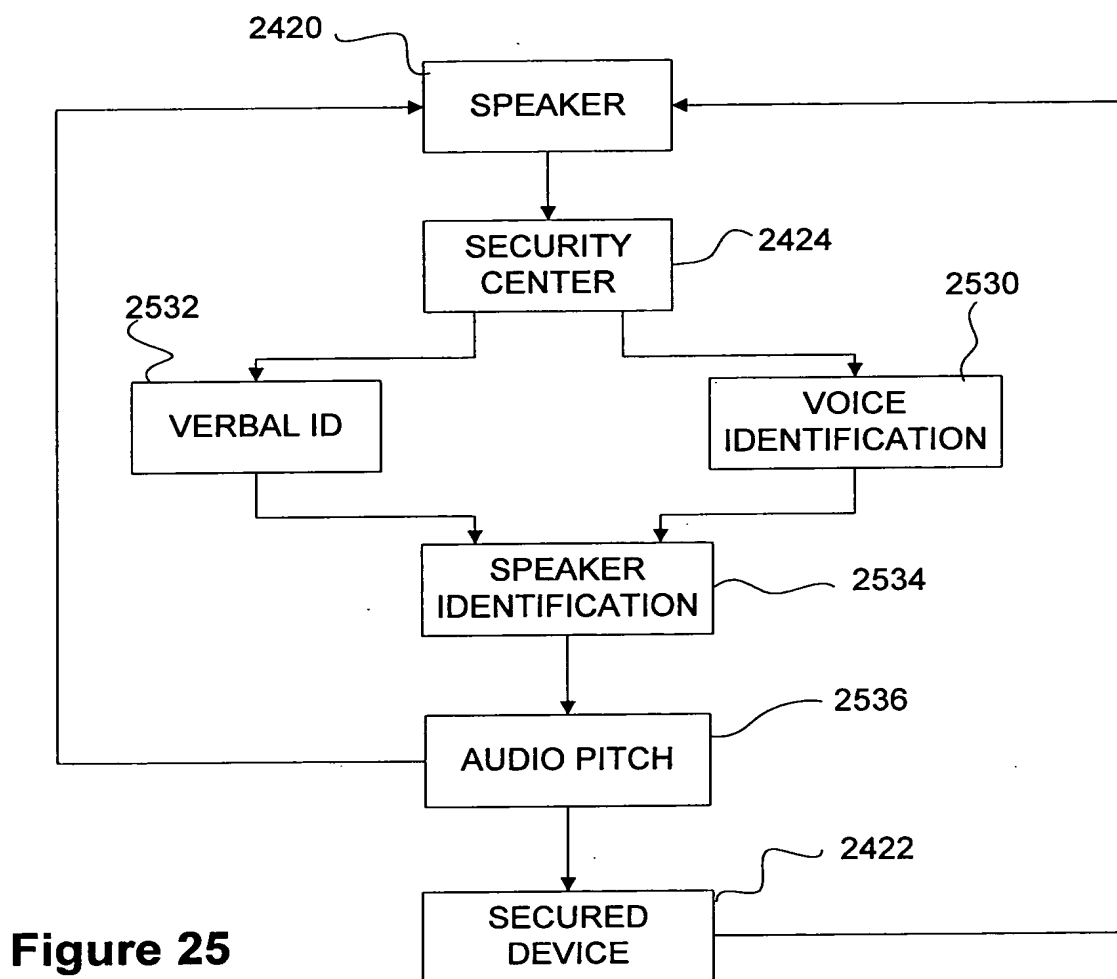
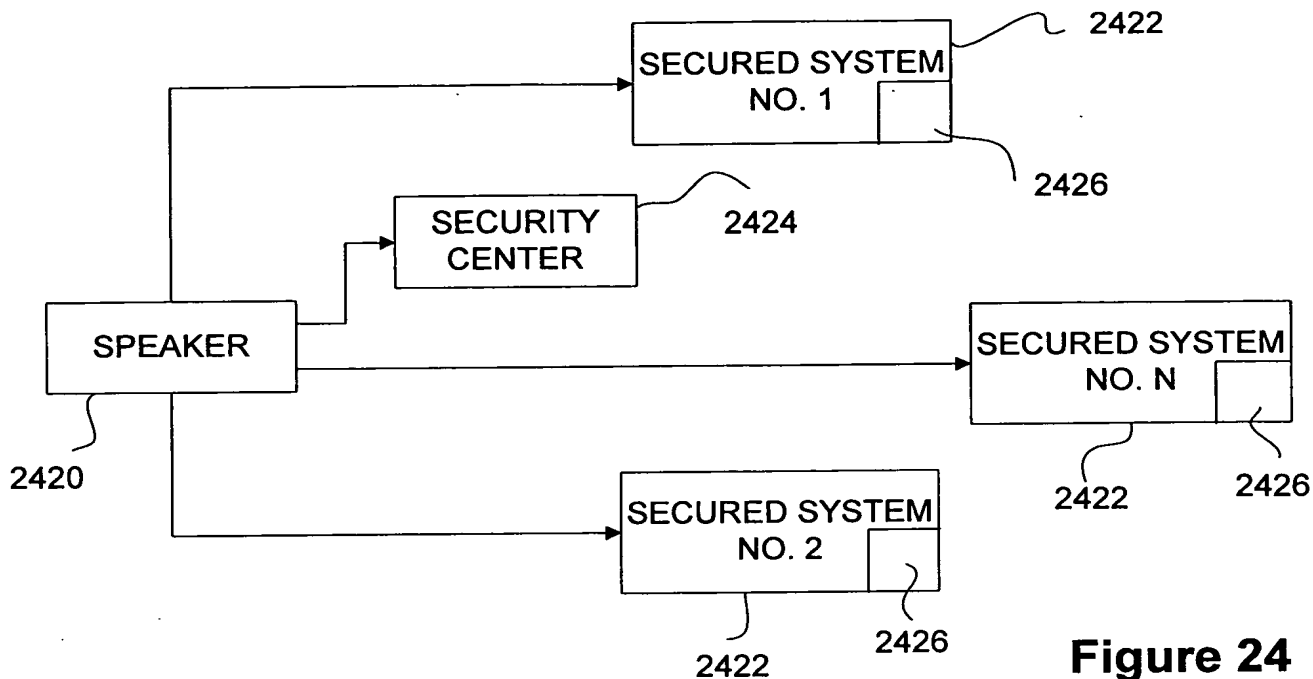
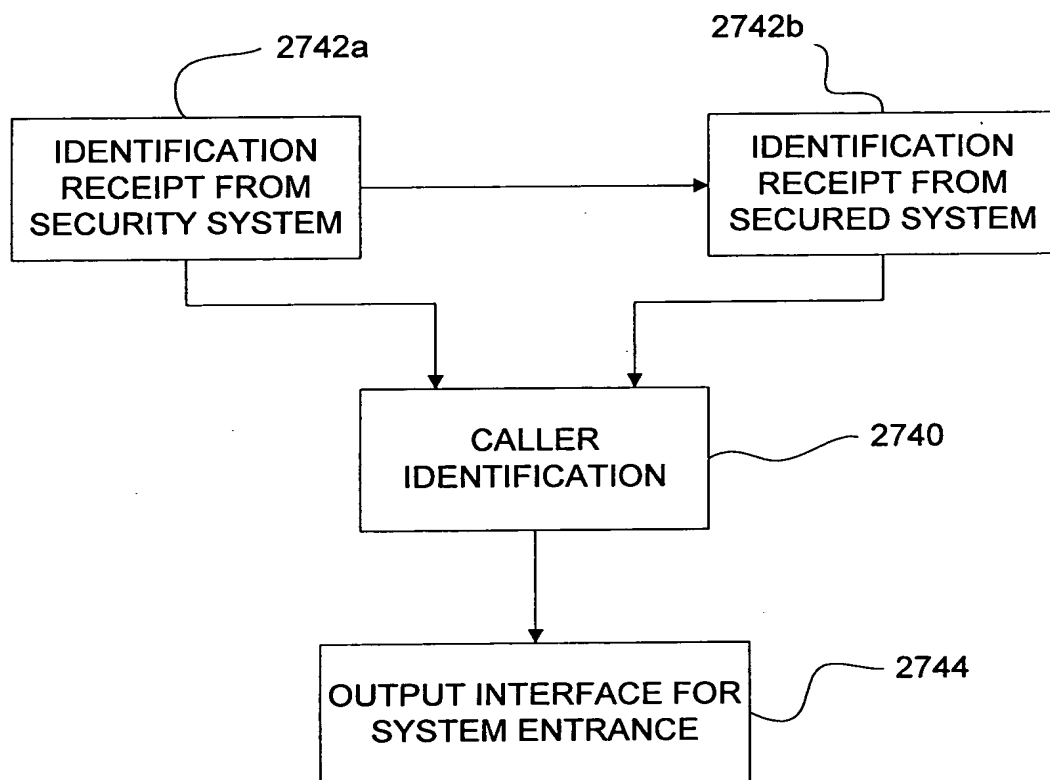
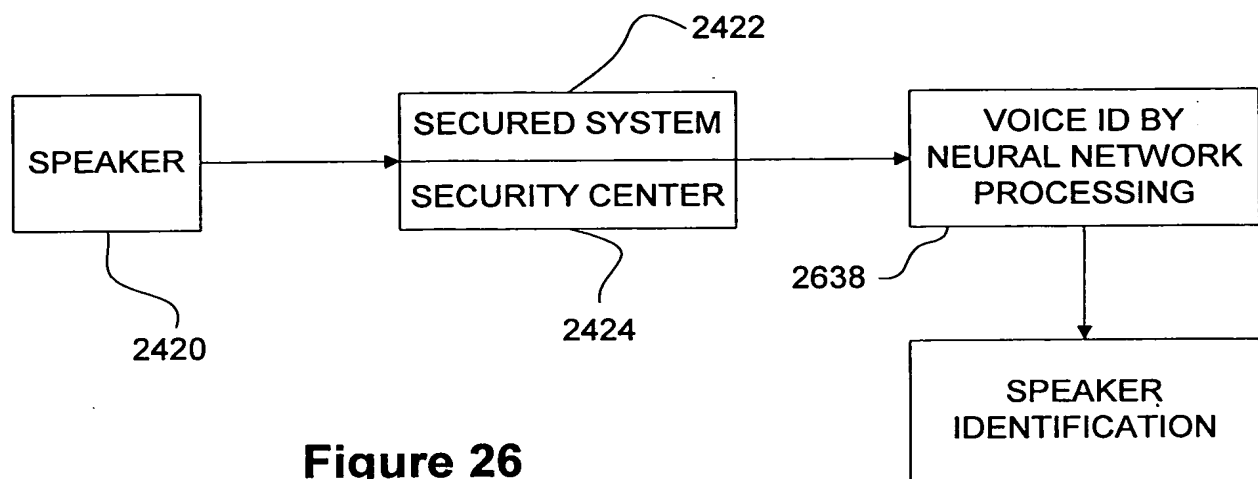


Figure 23





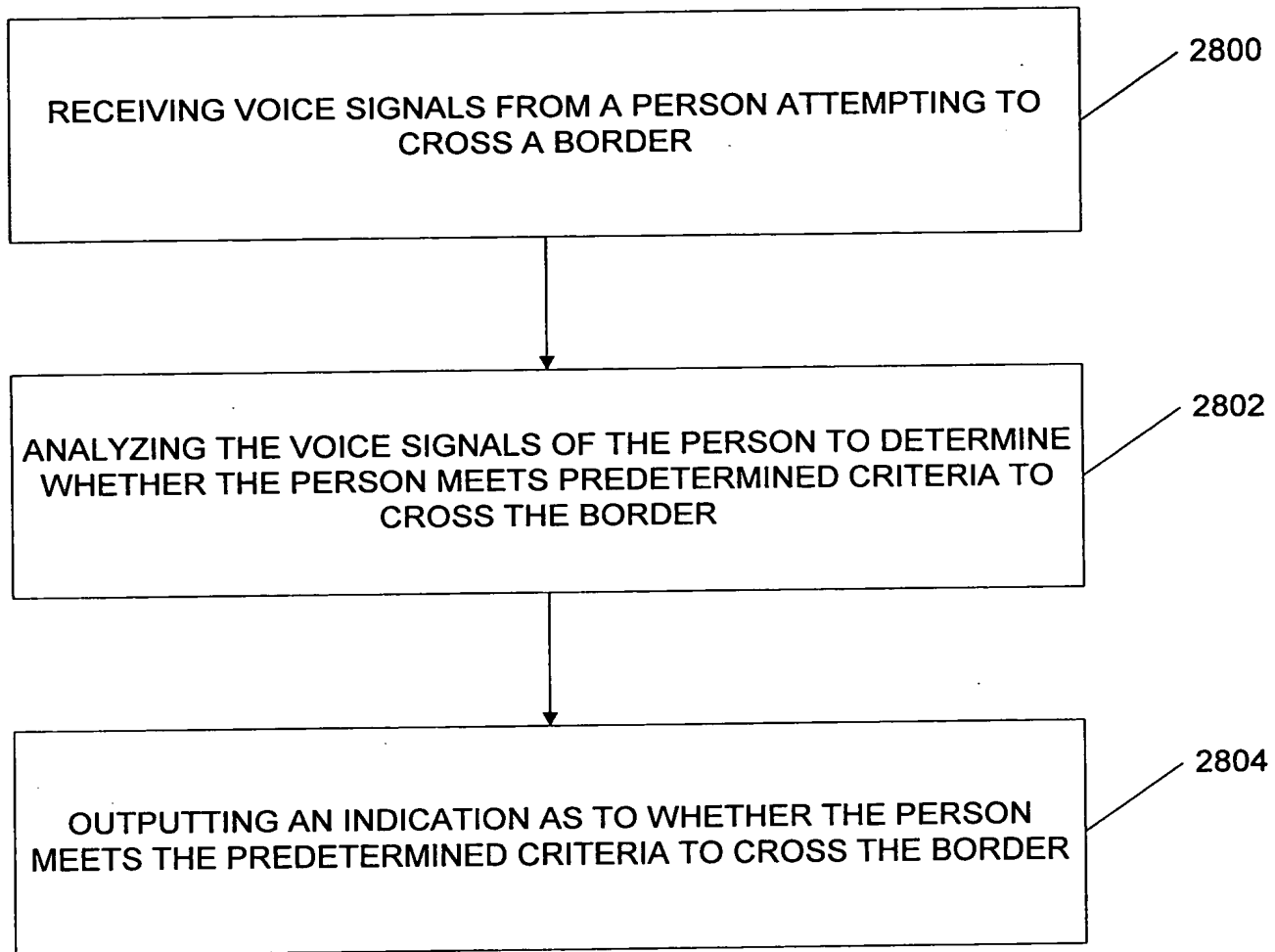


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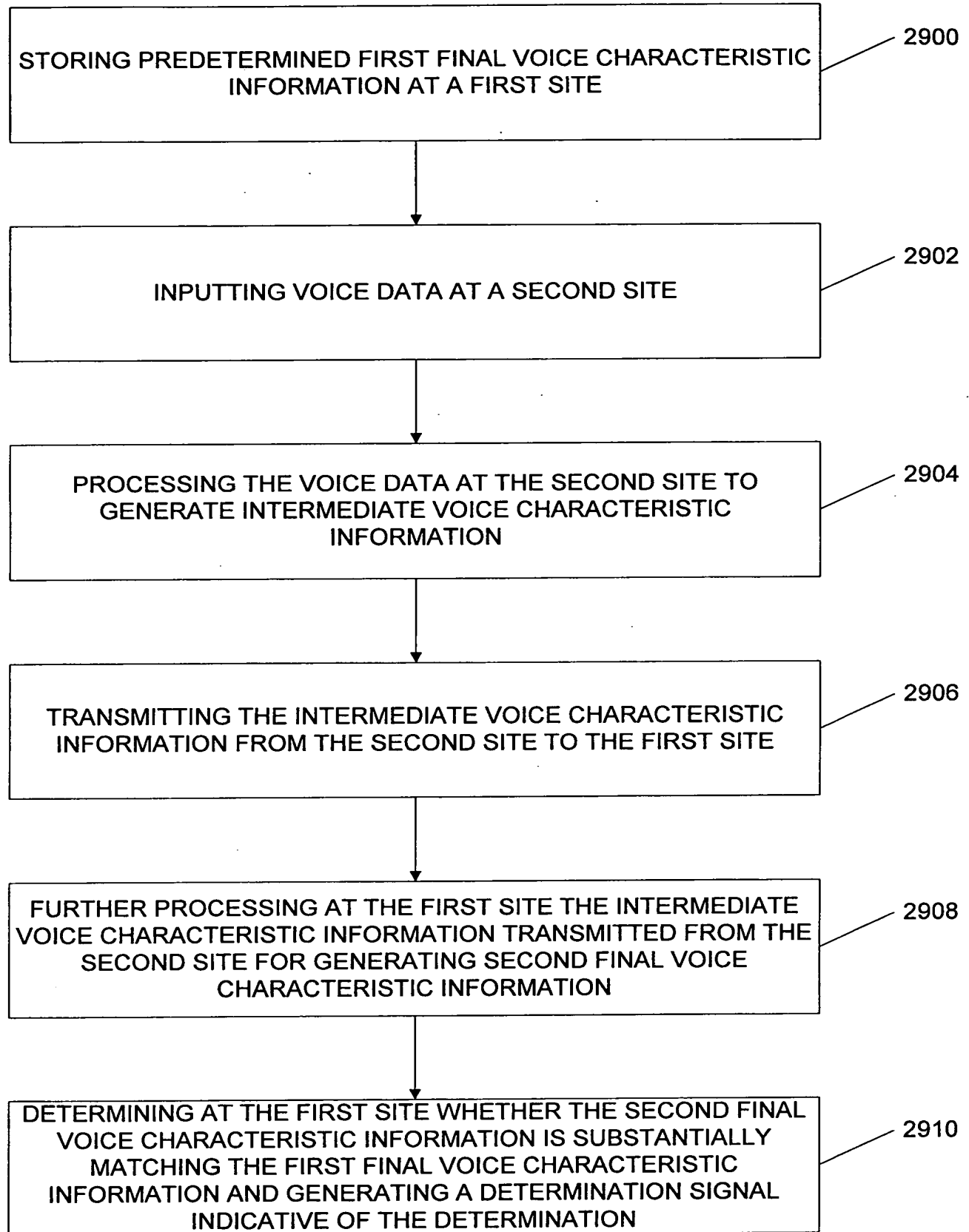


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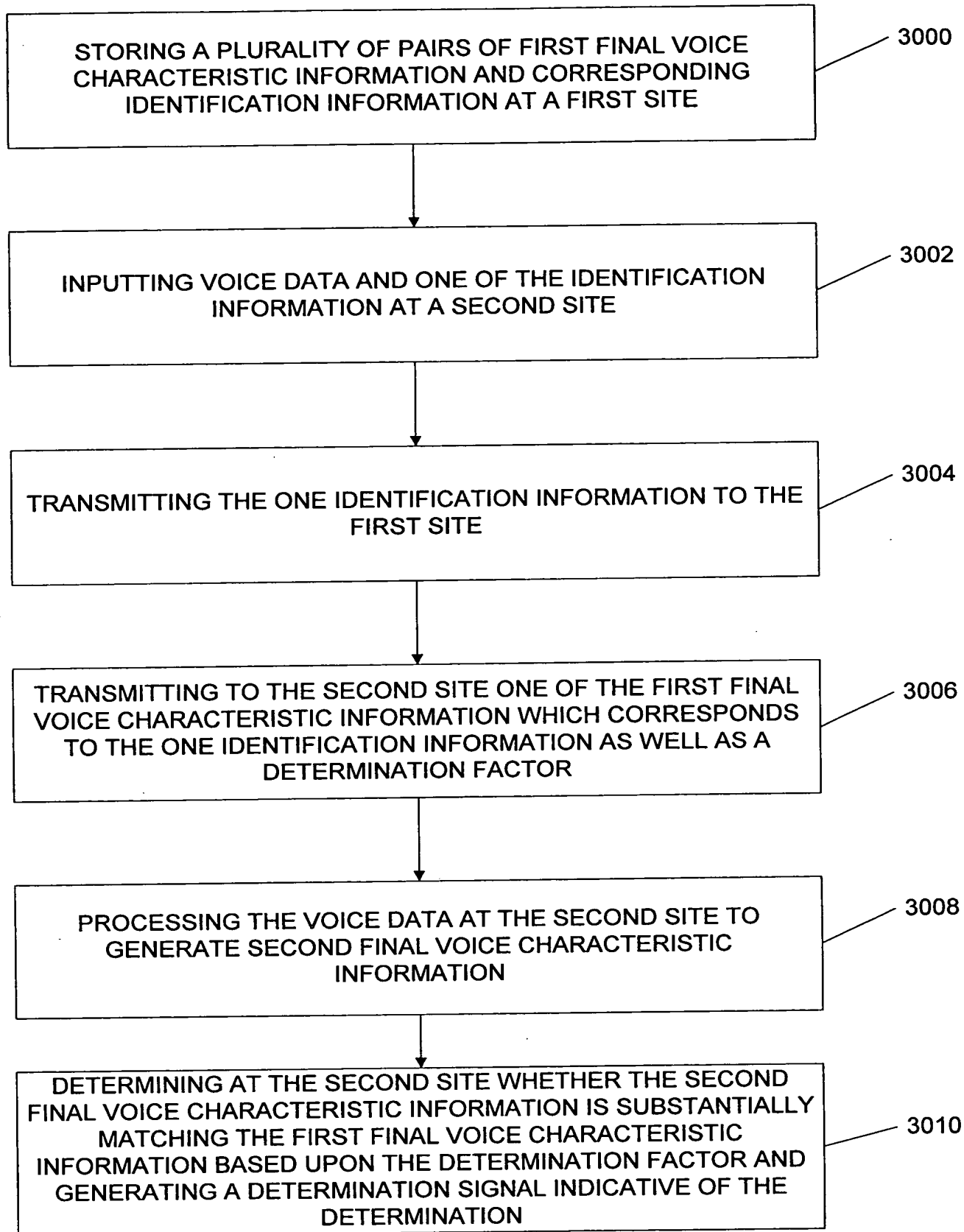


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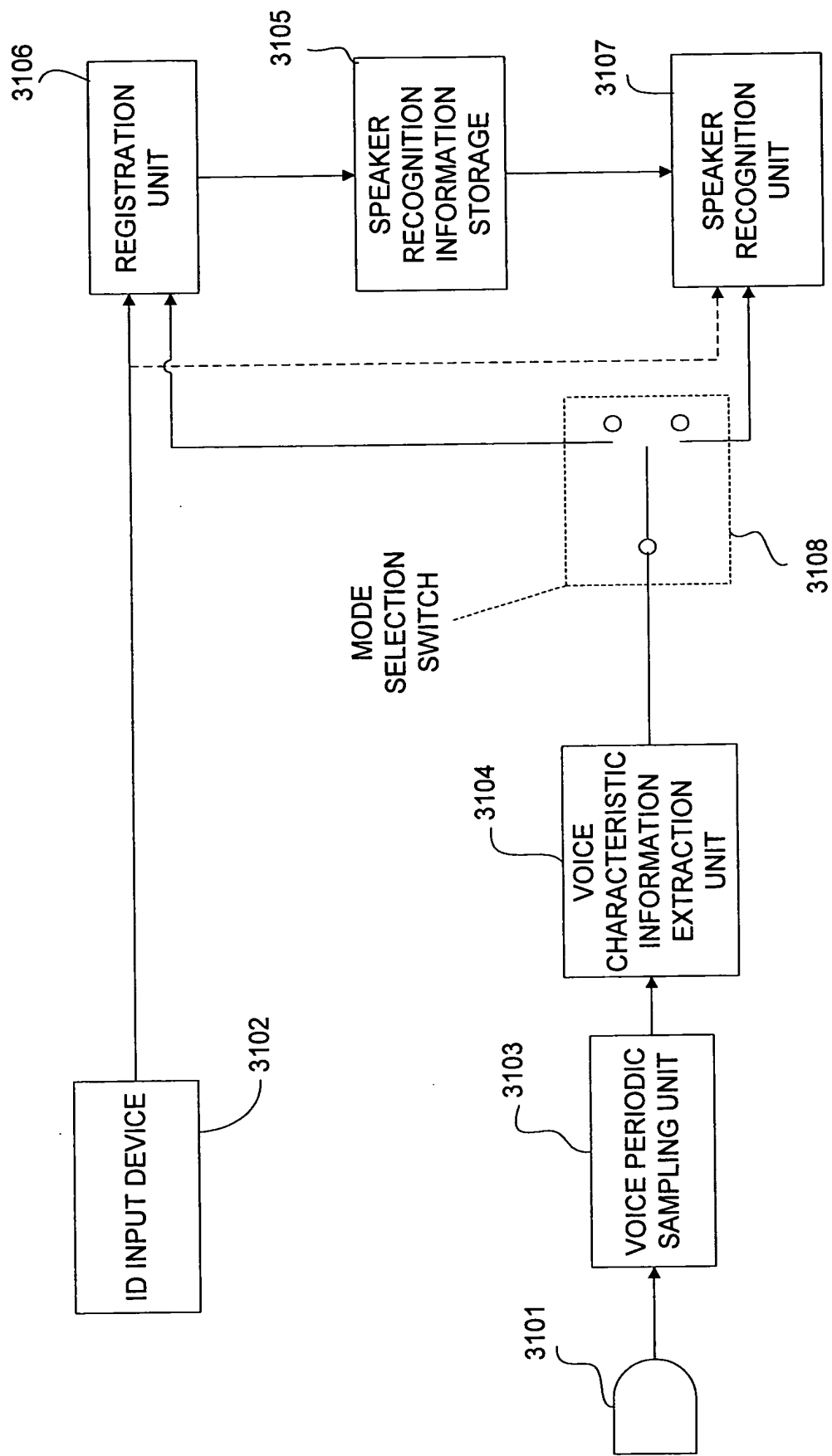


Figure 31



3105



IDENTIFICATION INFORMATION	STANDARD VOICE CHARACTERISTIC INFORMATION
A's ID	A's STANDARD PATTERN
B's ID	B's STANDARD PATTERN
C's ID	C's STANDARD PATTERN
D's ID	D's STANDARD PATTERN
⋮	⋮

Figure 32

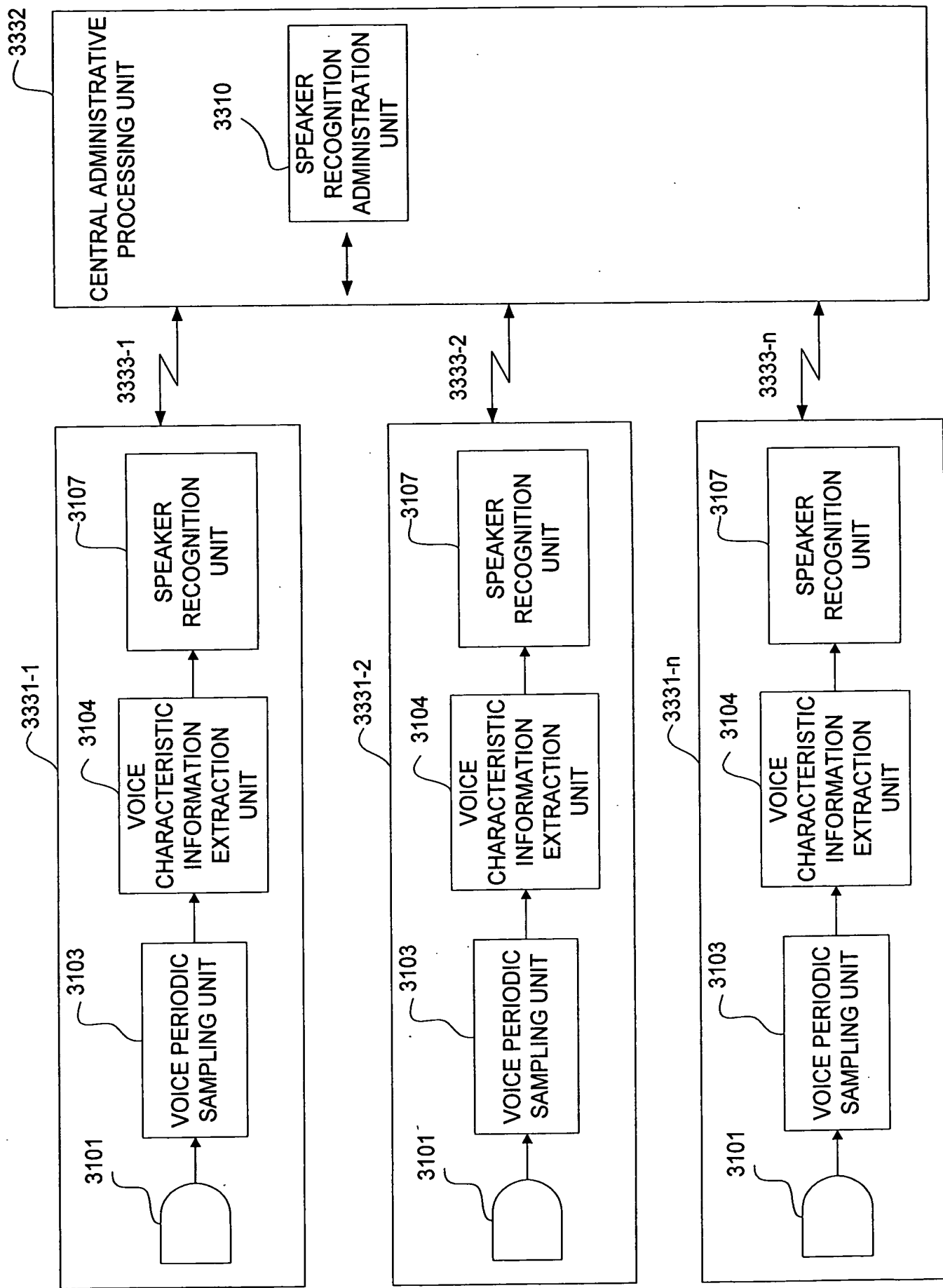


Figure 33

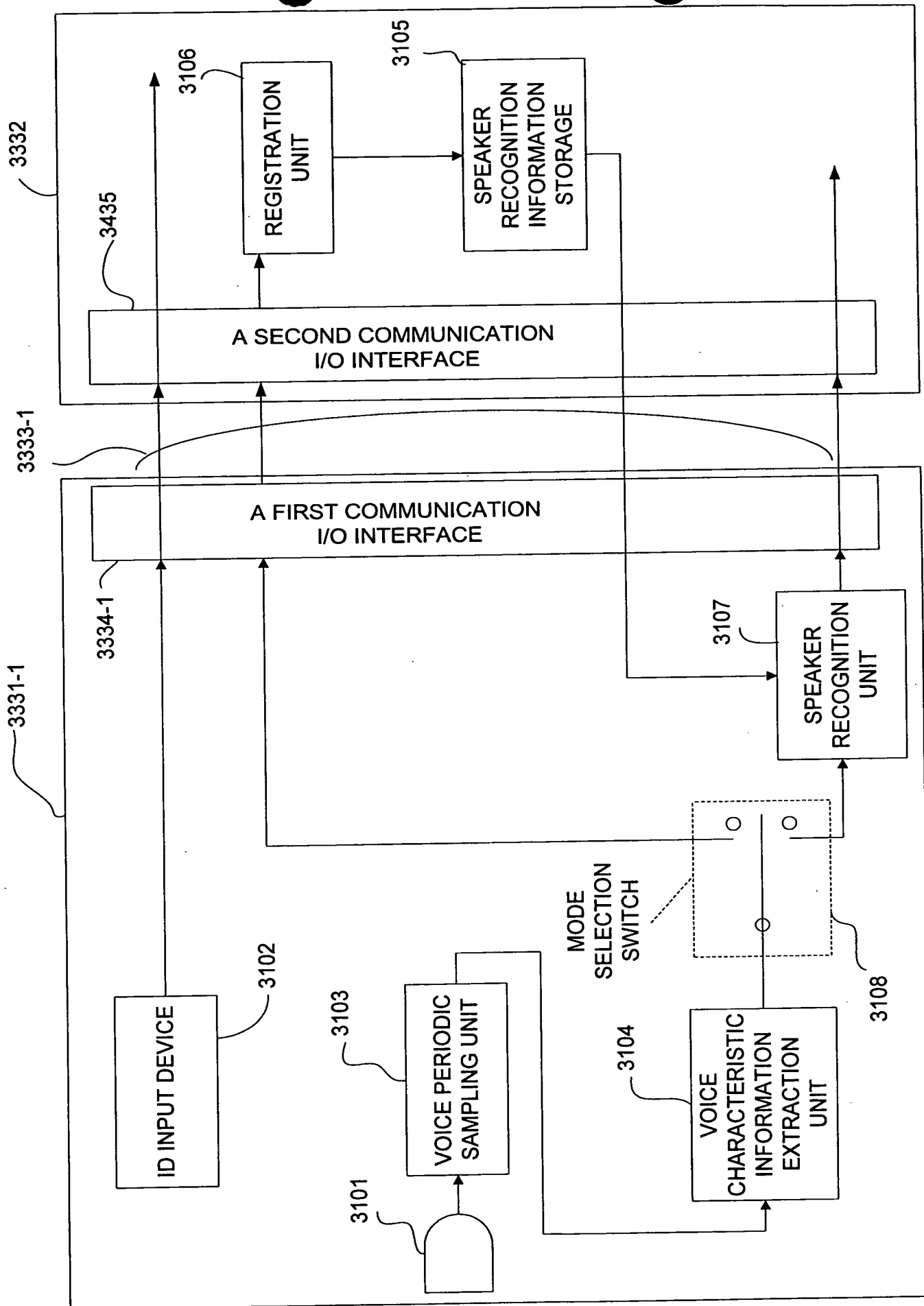


Figure 34

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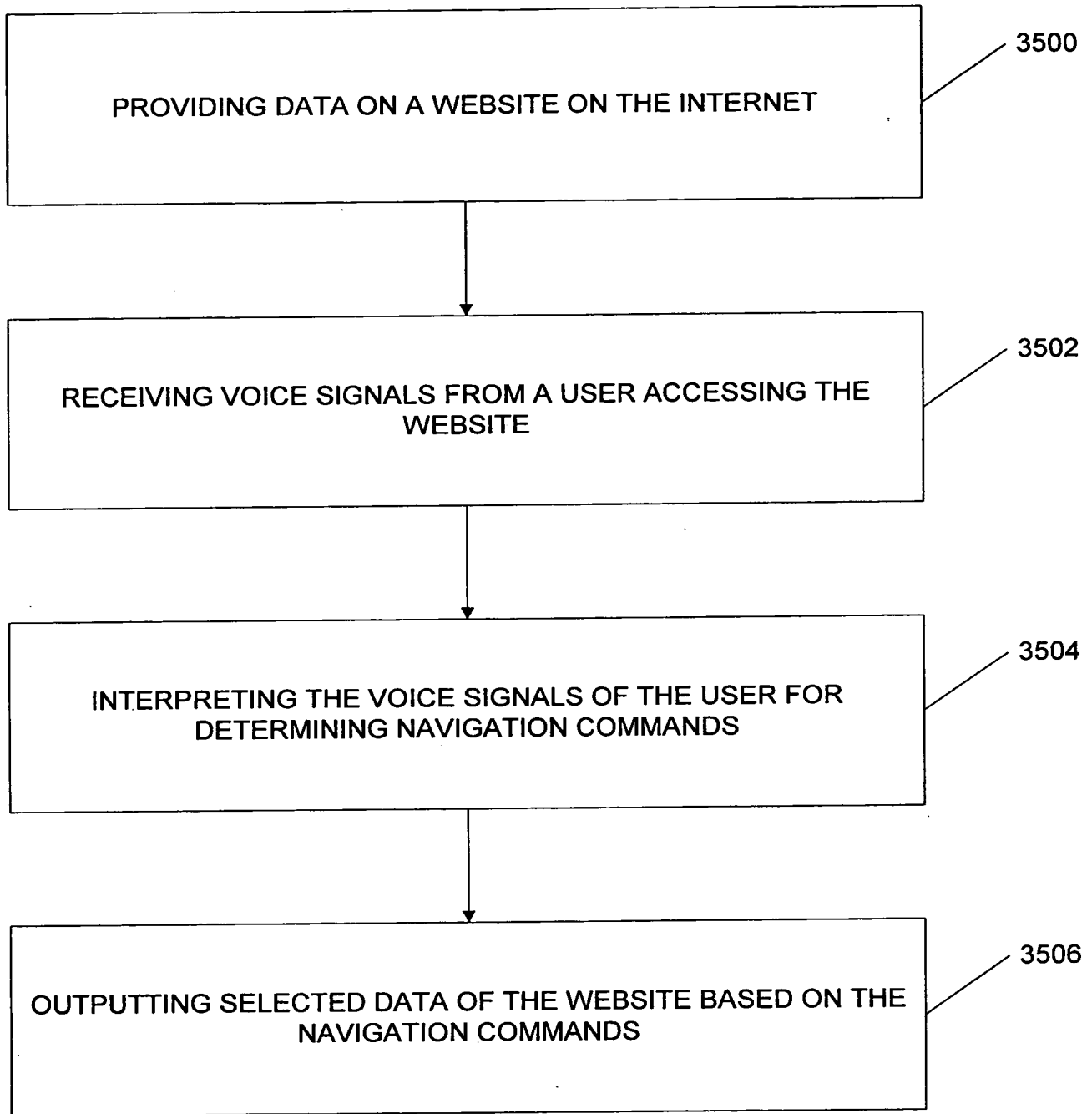


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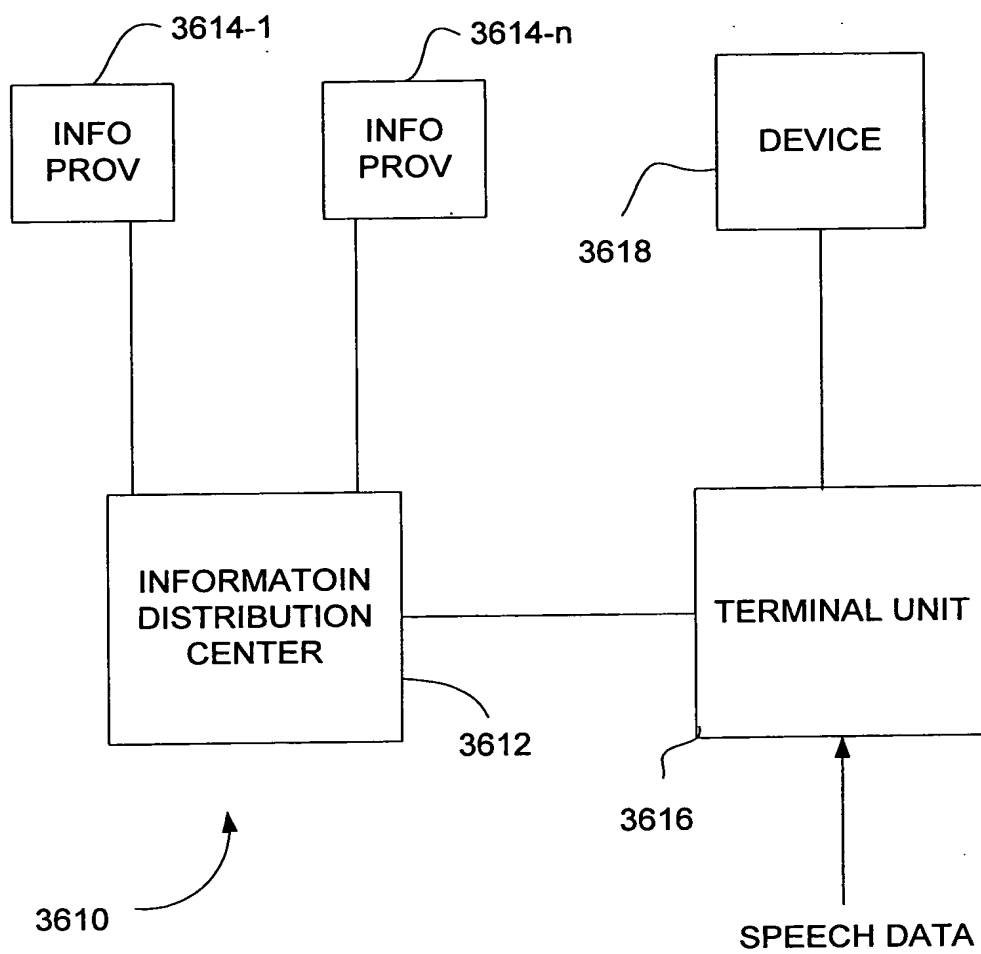


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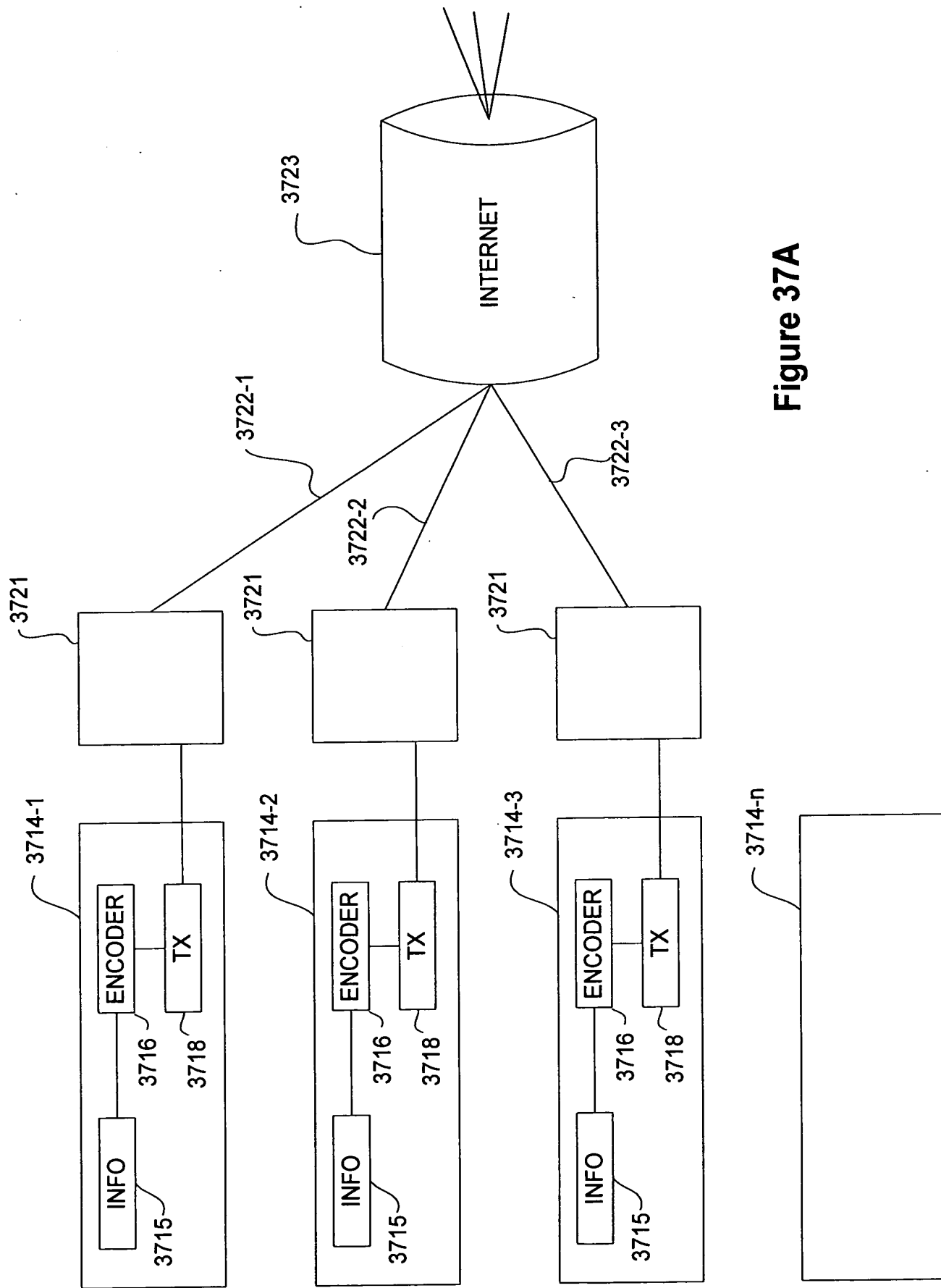


Figure 37A

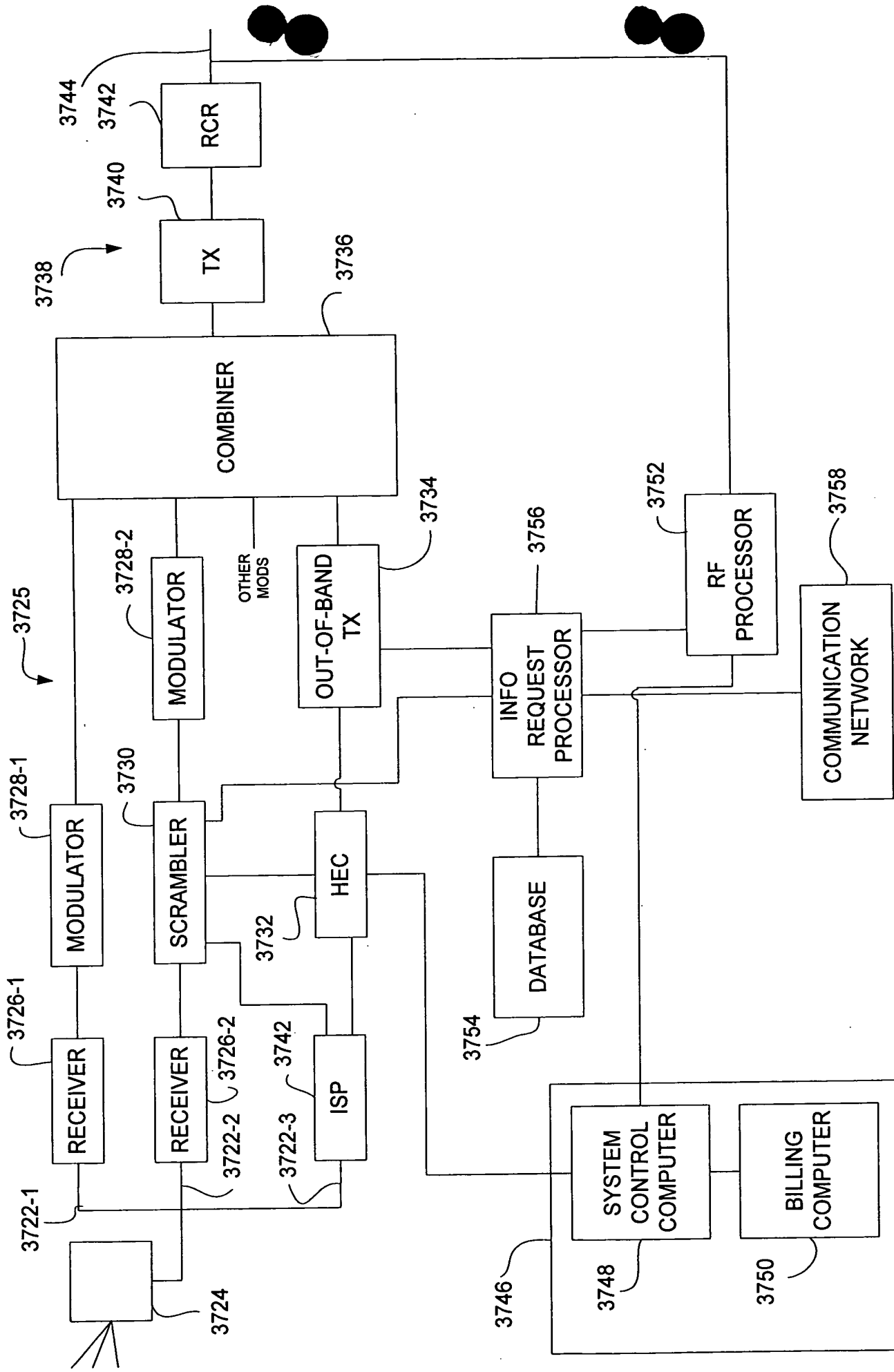


Figure 37B

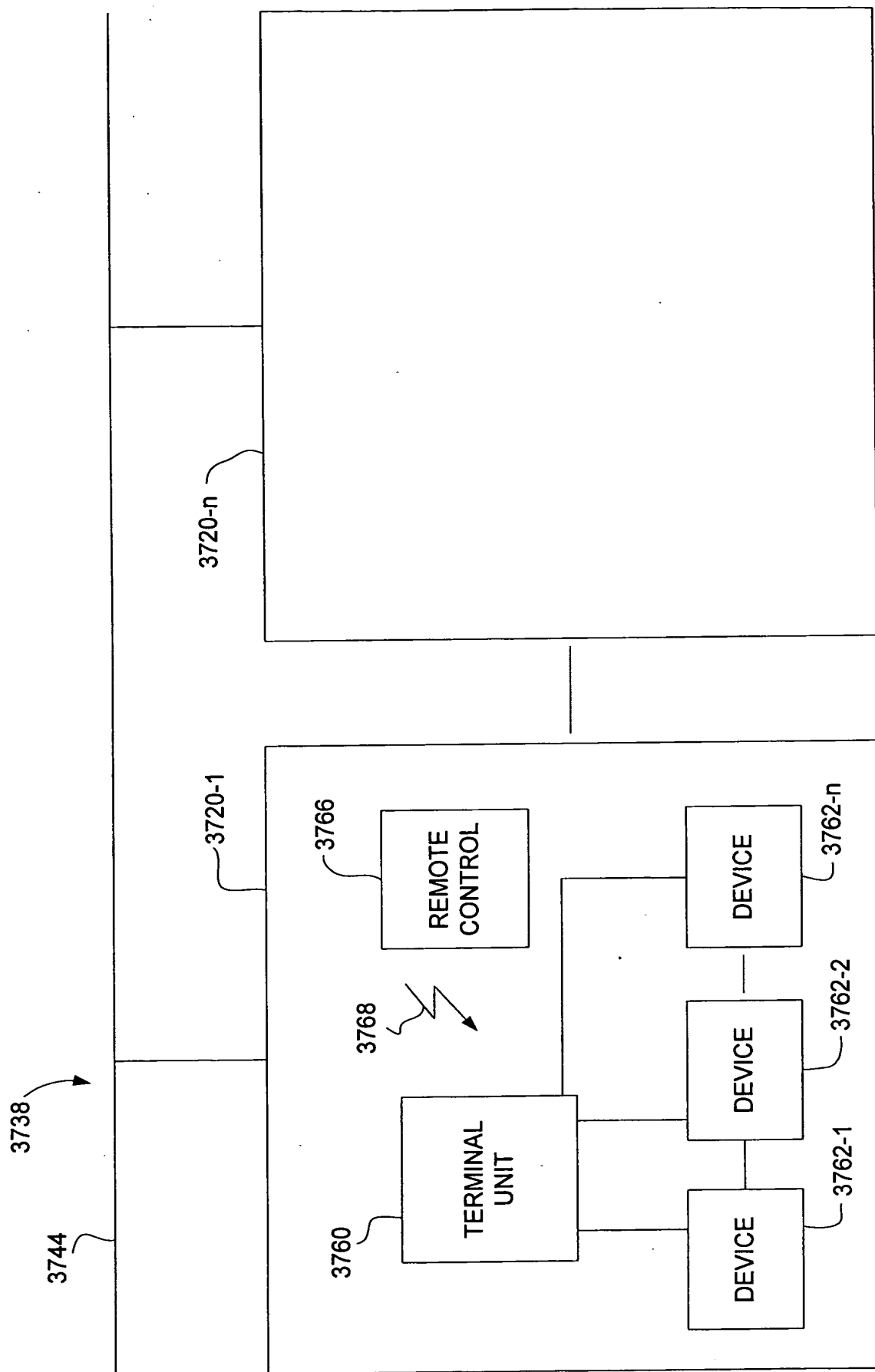


Figure 37C

67760 66660

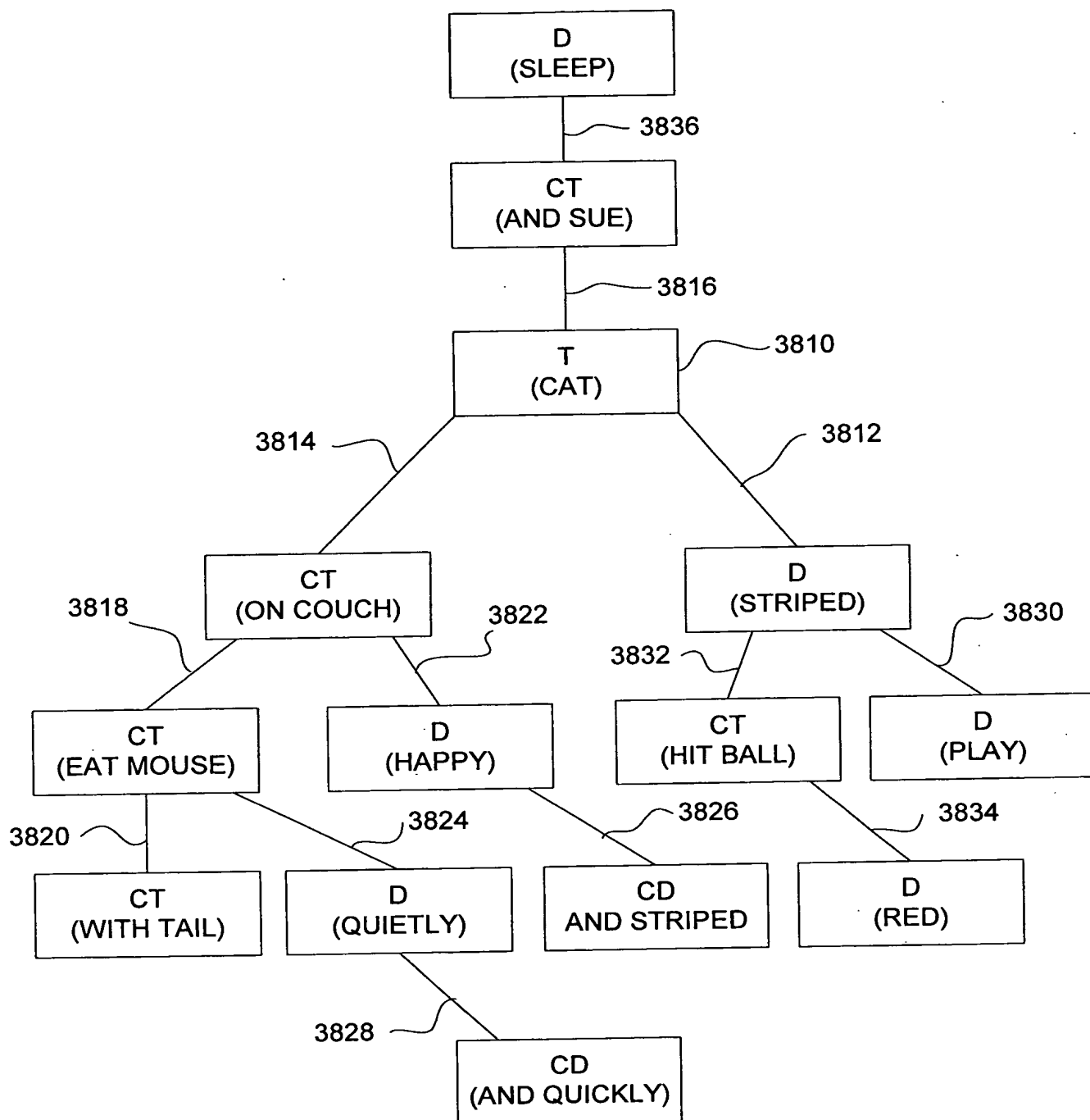


Figure 38

Figure 39

The diagram illustrates a system architecture centered around a **SYSTEM BUS 3900**. The bus connects several peripheral components: a **MASS STORAGE DEVICE** (3902), a **CPU** (3906), a **KEYBOARD** (3910), a **POSITION SENSING DEVICE** (3912), and a **SCREEN DISPLAY** (3914). A large functional block, labeled **3904**, is also connected to the system bus. This block contains the following sub-components and their interconnections:

- USER INTERFACE** (3930) is connected to the **OPERATING SYSTEM** (3920).
- The **OPERATING SYSTEM** (3920) is connected to the **ANALYSIS** (3925) module.
- The **ANALYSIS** (3925) module is connected to the **INPUT BUFFER** (3940) and the **OUTPUT BUFFER** (3945).
- The **INPUT BUFFER** (3940) and **OUTPUT BUFFER** (3945) are connected to the **ANALYSIS** (3925) module.
- The **ANALYSIS** (3925) module is connected to the **LINGUISTIC UNIT DATA-BASES** (3935).
- The **LINGUISTIC UNIT DATA-BASES** (3935) are connected to the **ANALYSIS** (3925) module.

The **SYSTEM BUS 3900** provides a communication pathway between the external components and the internal functional block 3904.